Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	4.75	kV

(T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D =12V R_L =33Ω	- -	MAX.	10	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	V_D =V_{DRM} T_j =125 R_L =3.3kΩ	- -	MIN.	0.2	V
I_L	I_G =1.2I_{GT}	-	MAX.	25	mA
				35	
I_H	I_T =100mA		MAX.	15	mA
dV/dt	V_D=540V Gate Open T_j				

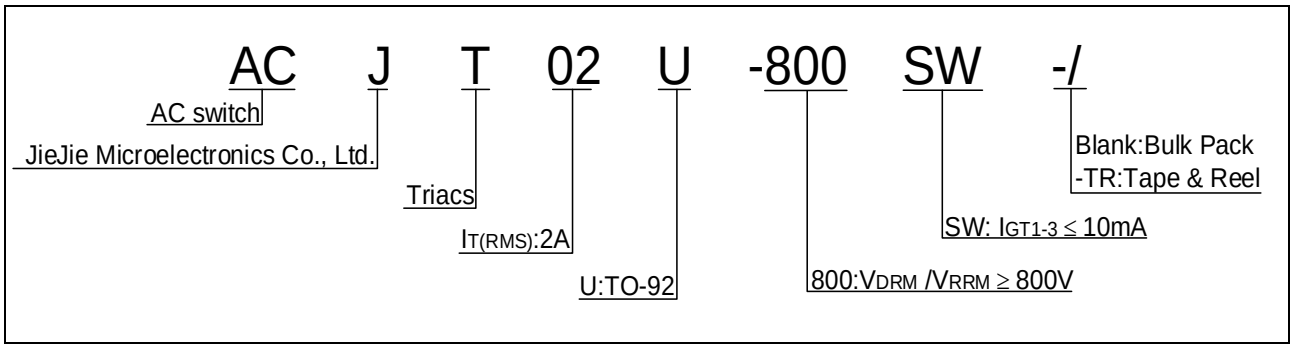

AC J T 02 U -800 SW -/
AC switch
JieJie Microelectronics Co., Ltd.
Triacs
IT(RMS):2A
U:TO-92
800:VDRM /VRRM ≥ 800V
SW: IGT1-3 ≤ 10mA
Blank:Bulk Pack
-TR:Tape & Reel

FIG.1: Maximum power dissipation versus RMS on-state current

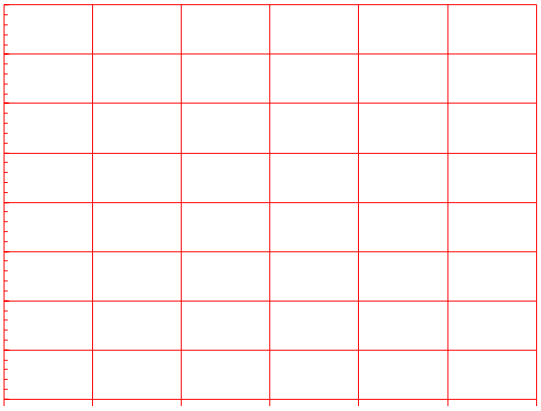
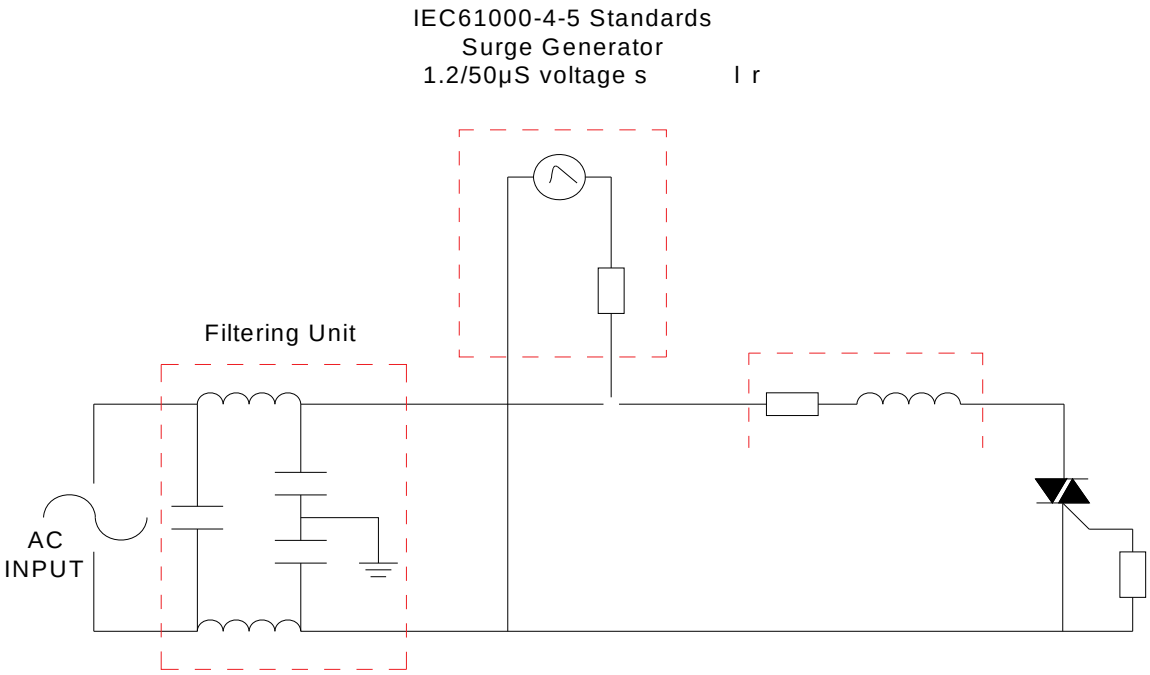


FIG.2: RMS on-state current versus case temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
ACJT02U-800SW	800	10	TO-92	1,000	Bulk Pack
ACJT02U-800SW-TR				2,000	Tape & Reel

Document Revision History

Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated
Mar.27, 2025	A.2.0	Renew PACKAGE MECHANICAL DATA
Sept.28, 2025	A.2.1	Revise PACKAGE MECHANICAL DATA

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