Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.8)	V_{pp}	3.5	kV (]TJ ET Q q 5

FIG.1: Maximum power dissipation versus RMS on-state current

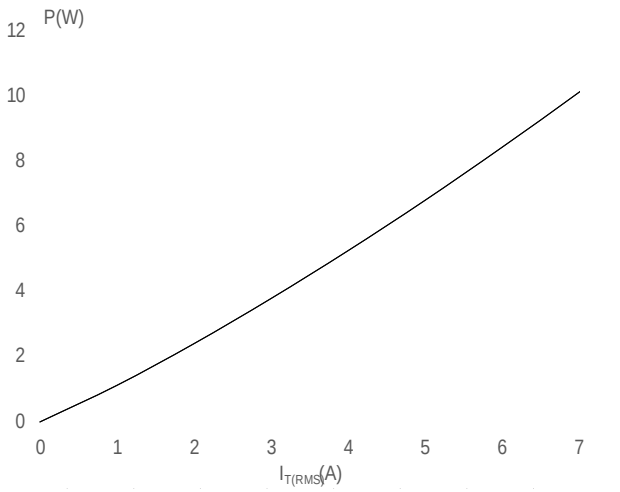


FIG.2: RMS on-state current versus case temperature

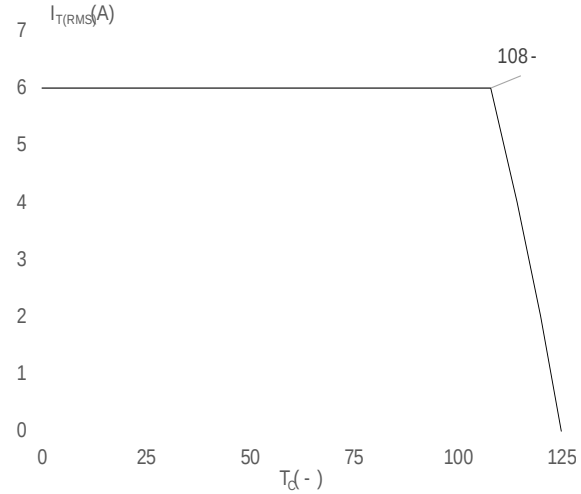


FIG.3: RMS on-state current versus ambient temperature (printed circuit board FR4,copper

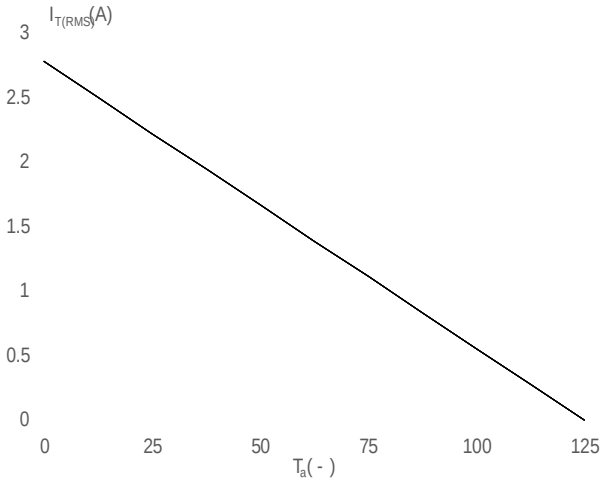
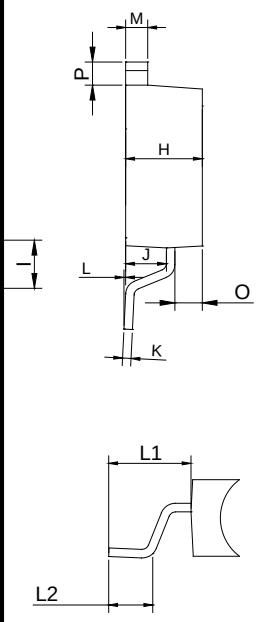


FIG.4: Surge peak on-state current versus number of cycles

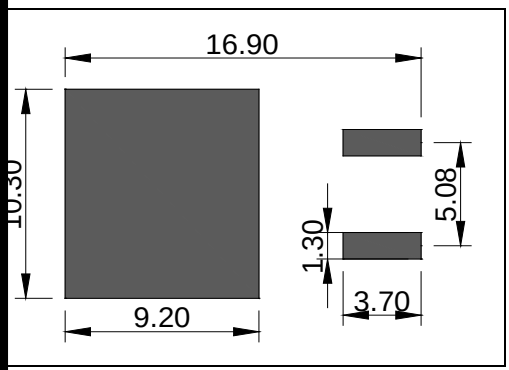
FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

L DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	9.90		10.20	0.390		0.402
A1	0.80		1.20	0.031		0.047
B	14.70		15.80	0.579		0.622
B1	2.15		2.55	0.085		0.100
C	9.40		9.60	0.370		0.378
C1	2.40		2.80	0.094		0.110
D	2.40		2.70	0.094		0.106
E	1.20		1.50	0.047		0.059
F	0.75		0.85	0.029		0.033
G	1.00		1.50	0.039		0.059
H	4.40		4.70	0.173		0.185
I						
J	2.30		2.70	0.091		0.106
K	0.38		0.55	0.015		0.022
L	0	0.10	0.25	0	0.004	0.010
L1	4.65		5.05	0.183		0.199
L2	1.90			0.075		
M	1.25		1.35	0.049		0.053
P	1.20		1.50	0.047		0.059

nsions in mm)



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