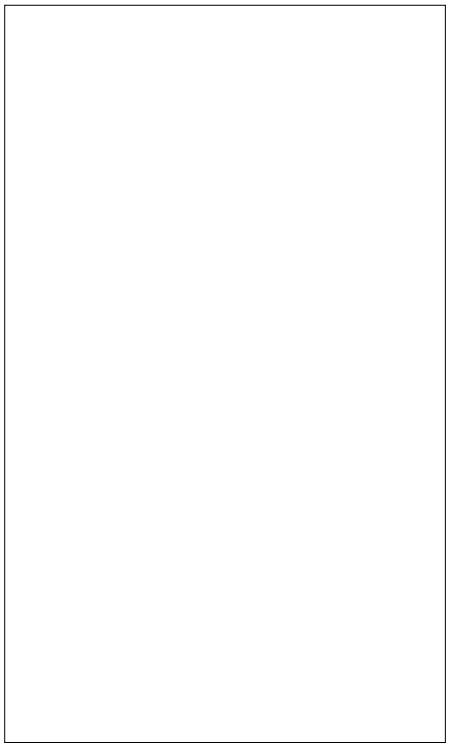
ose
on in

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210-6W
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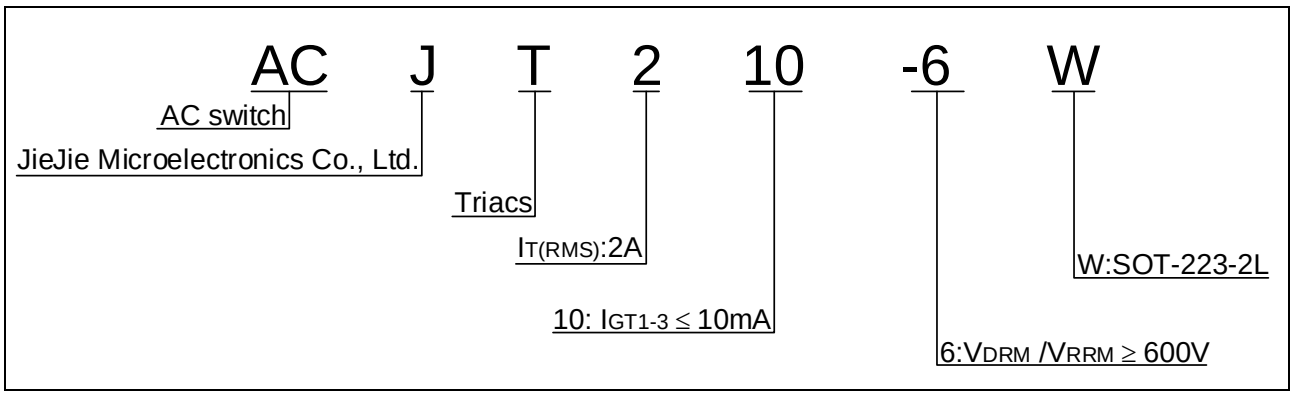
	ymbol	Value	Unit
	T_{stg}	-40-150	
	T_j	-40-125	
	V_{DRM}	600	V
Repetitive peak reverse voltage (T_j=25)	V_{RRM}	600	V
RMS on-state current ($T_C \leq 76$)	$I_{T(RMS)}$	2	A

Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.8)	V_{pp}	4.5	kV

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value	Unit
I_{GT}	V_D =12V R_L =33Ω	- -		

ORDERING INFORMATION



MARKING

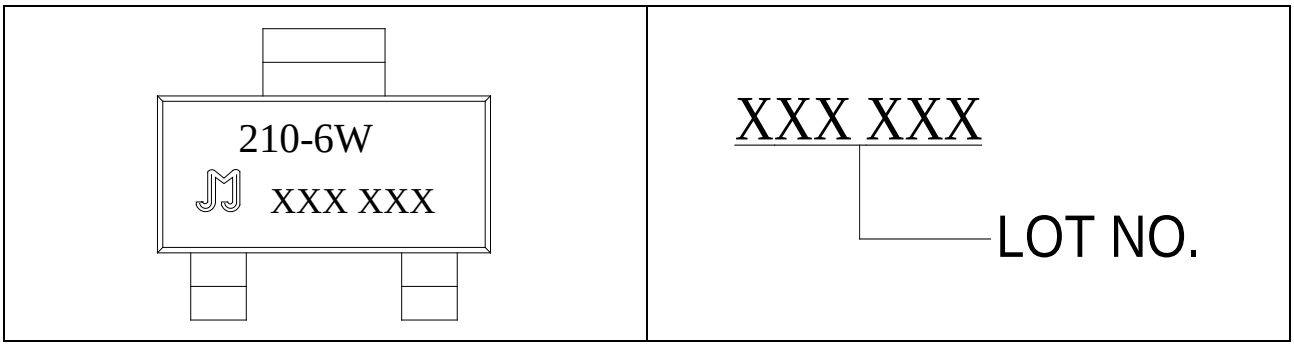


FIG.1: Maximum power dissipation versus RMS on-state current

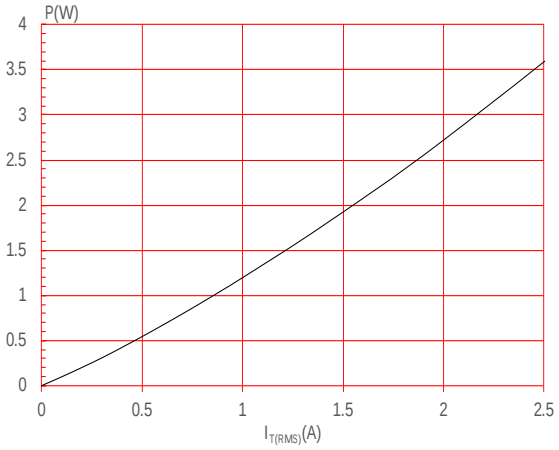


FIG.2: RMS on-state current versus case temperature

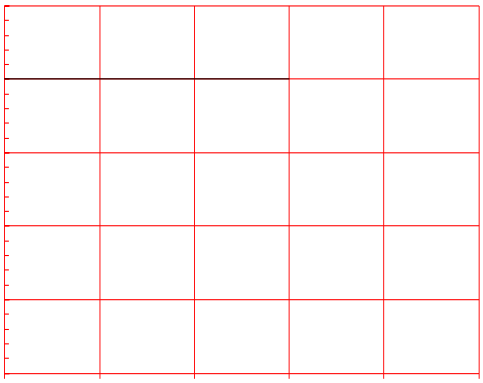


FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

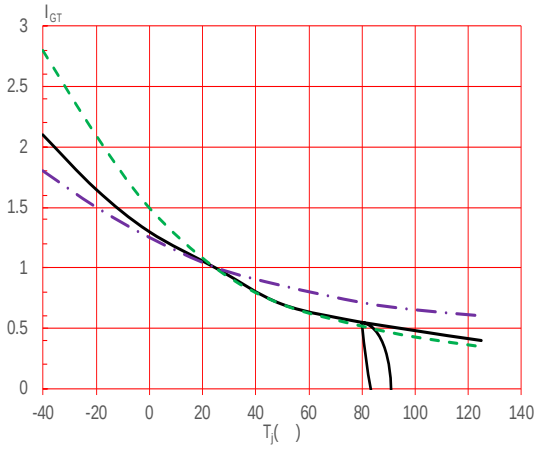
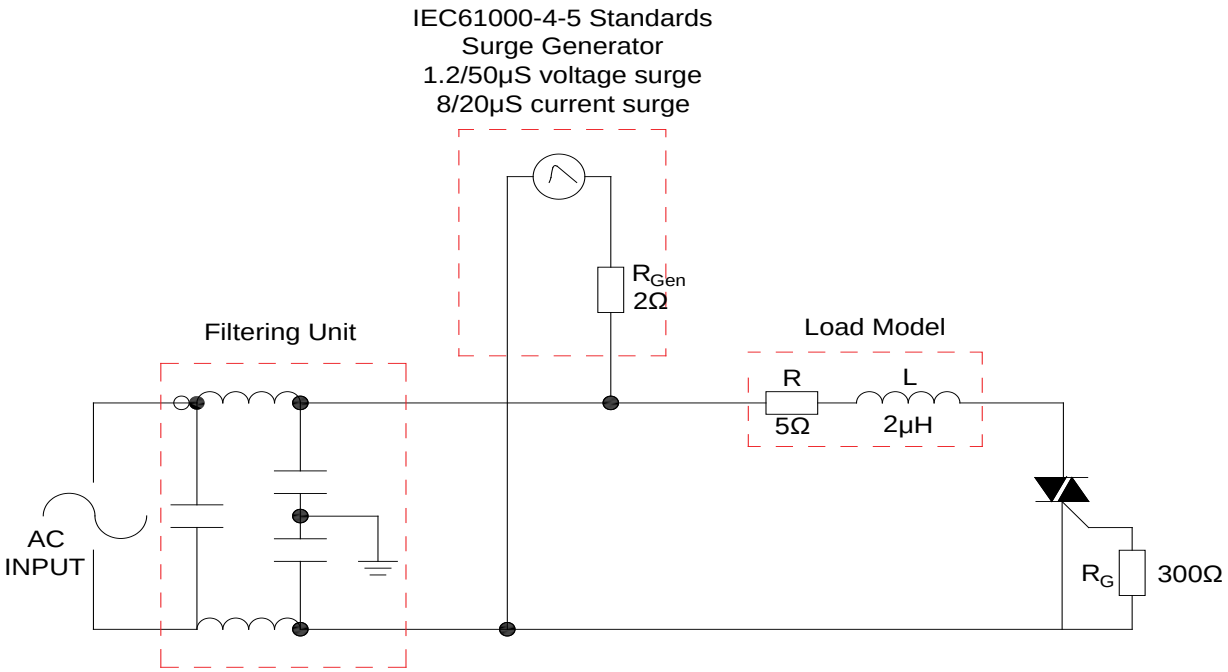


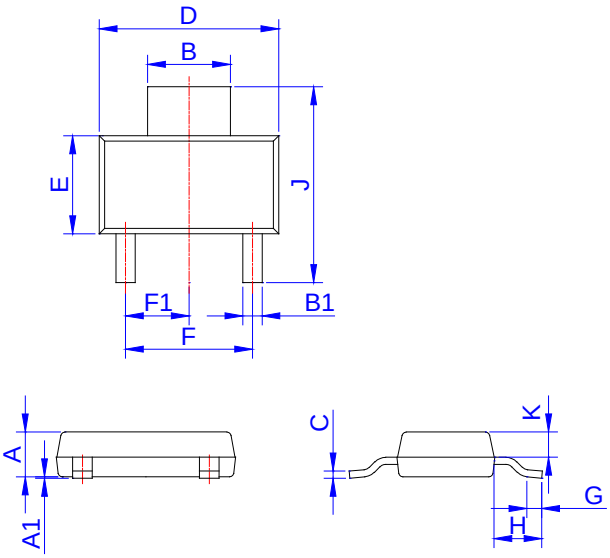
FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode a75. 5.3212 753

PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	1.50	1.60	1.80	0.059	0.063	0.071
A1	0.01		0.10			
B	2.90		3.10	0.114		0.122
B1	0.60	0.70	0.80	0.024	0.028	0.031
C	0.22	0.254	0.32	0.009	0.010	0.013
D	6.30	6.50	6.70	0.248	0.256	0.264
E	3.30	3.50	3.70	0.130	0.138	0.146
F	4.40			0.173		
F1	2.20			0.087		
G						
H	1.50	1.75	2.00	0.059	0.069	0.079
J	6.70	7.00	7.30	0.264	0.276	0.287
K	0.80			0.031		

ACJT2