Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	3.5	kV

(T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D =12V R_L =33Ω	- -	MAX.	25	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	V_D =V_{DRM} T_j=125 R_L =3.3kΩ	- -	MIN.	0.2	V
I_L	I_G =1.2I_{GT}	-	MAX.		

AC J T 4 25 -8 H

- AC: AC switch
- J: JieJie Microelectronics Co., Ltd.
- T: Triacs
- 4: $I_{T(RMS)}$:4A
- 25: $I_{GT1-3} \leq 25mA$
- -8: V_{DRM} /$V_{RRM} \geq 800V$
- H: H:TO-251

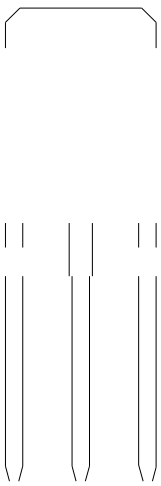
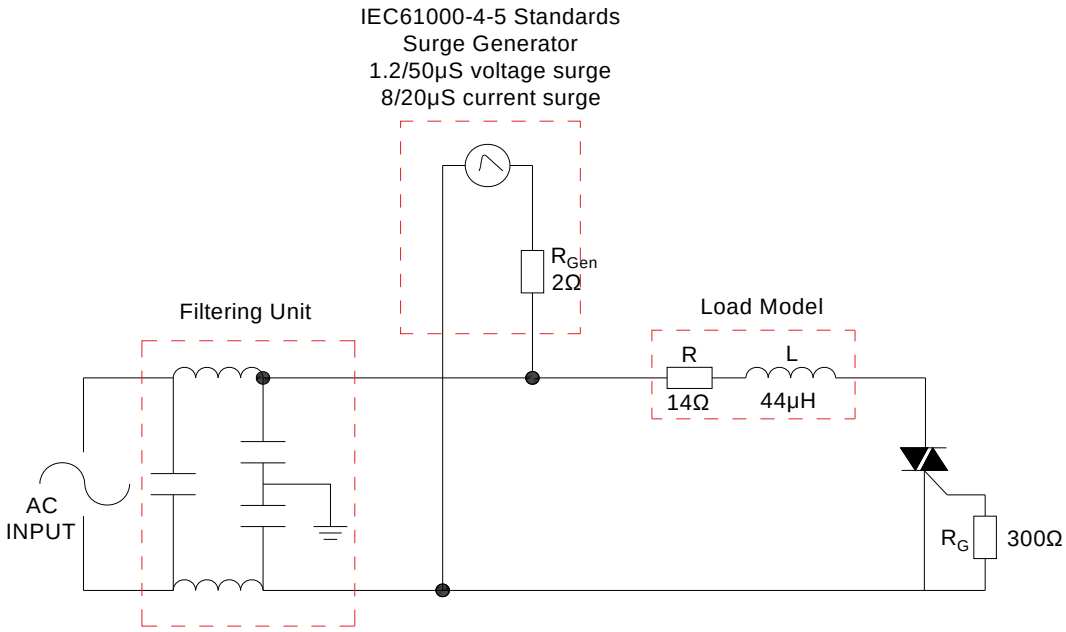


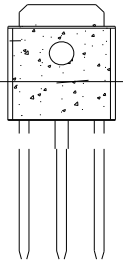
FIG.1: Maximum power dissipation versus RMS on-state current

FIG.2: RMS on-state current versus case temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



	Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package



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