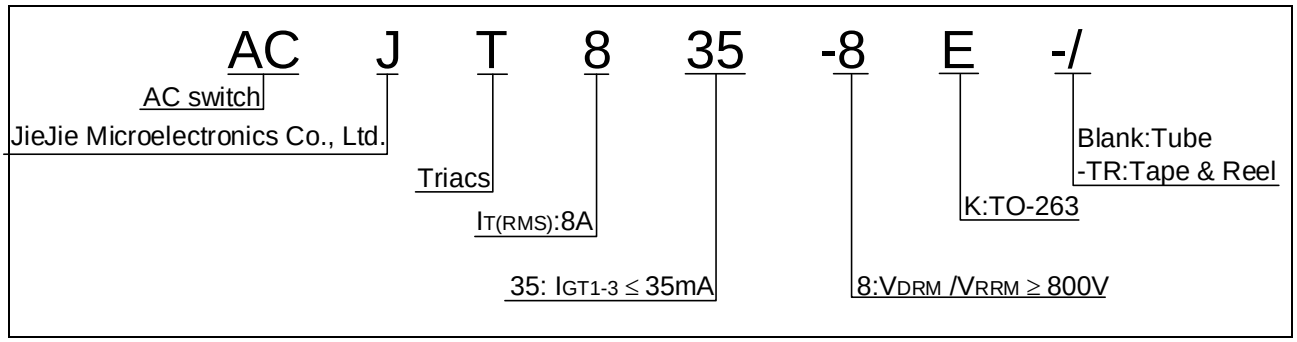
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.8)	V_{pp}	2.5	kV

(T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D =12V R_L =33Ω	- -	MAX.	35	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	V_D =V_{DRM} T_j =125 R_L =3.3kΩ	- -	MIN.	0.2	V
I_L	I_G =1.2I_{GT}	-	MAX.	35	mA
				55	
I_H	I_T =100mA		MAX.	30	mA
dV/dt	V_D=540V Gate Open T_j =125		MIN.	2600	V/μs
(dI/dt)c	(dV/dt)c=10V/μs, T_j=125		MIN.	10	A/ms
t_{on}	I_G=40mA I_A=200mA I_R=20mA T_j=25		TYP.	3	μs
t_{off}				30	
V_{CL}	I_{CL}=0.1mA t_p=1ms		MIN.	850	V

Symbol	Parameter	Value(MAX.)	Unit
V_{TM}	I_{TM} =10		


AC J T 8 35 -8 E -/
AC switch
JieJie Microelectronics Co., Ltd.
Triacs
IT(RMS):8A
35: IGT1-3 ≤ 35mA
8:VDRM /VRRM ≥ 800V
K:TO-263
Blank:Tube
-TR:Tape & Reel

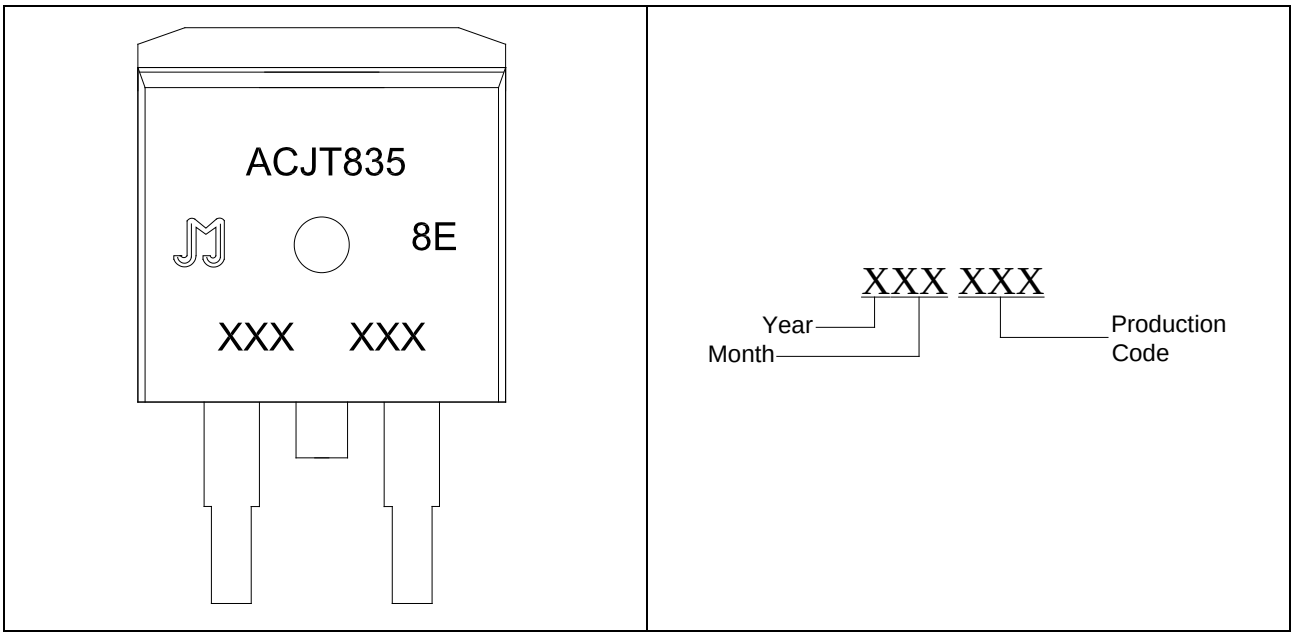

ACJT835
8E
XXX XXX
XXX XXX
Year
Month
Production
Code

FIG.1: Maximum power dissipation versus RMS on-state current

FIG.2: RMS on-state current versus case temperature

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

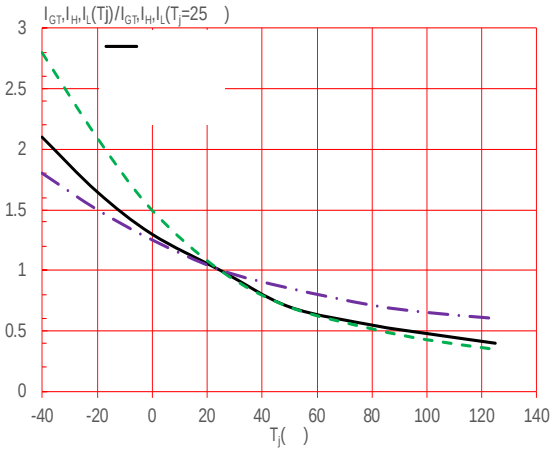
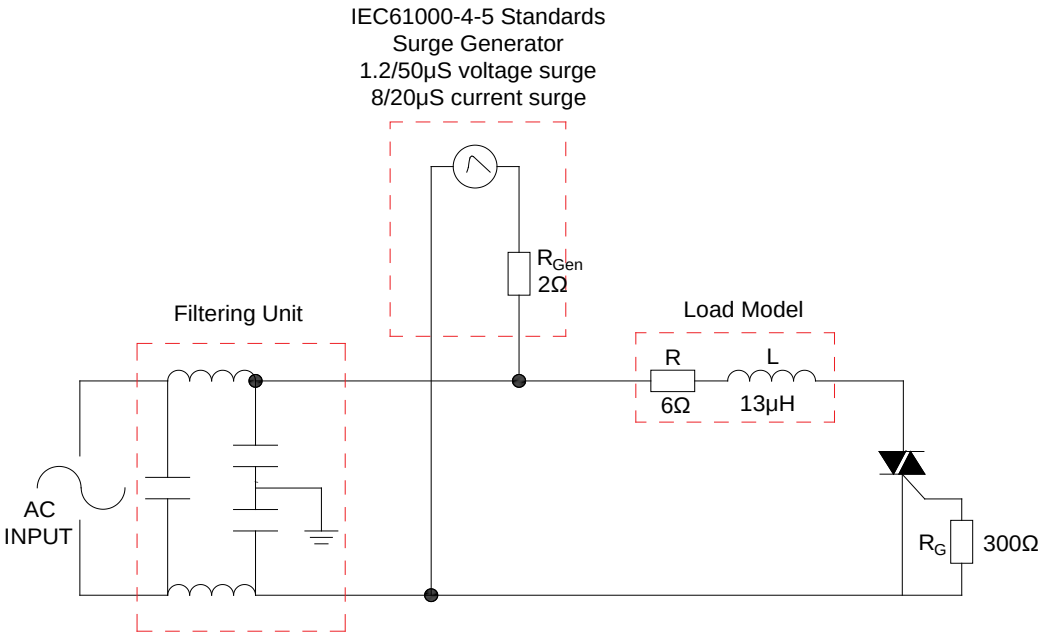
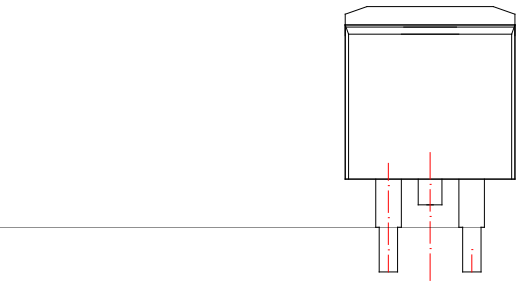
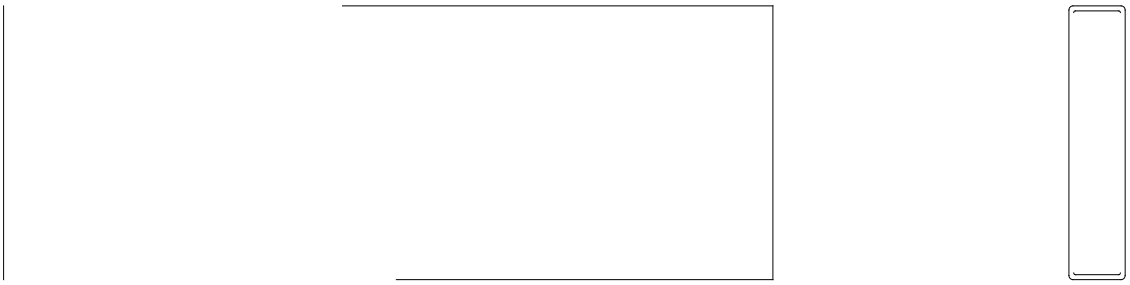


FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards





Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	9.90		10.20	0.390		0.402
B	14.70		15.80	0.579		0.622
C	9.40		9.60	0.370		0.378
D	2.40			0.094		
E	1.20		1.50	0.047		0.059
F	0.75		0.85	0.029		0.033



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