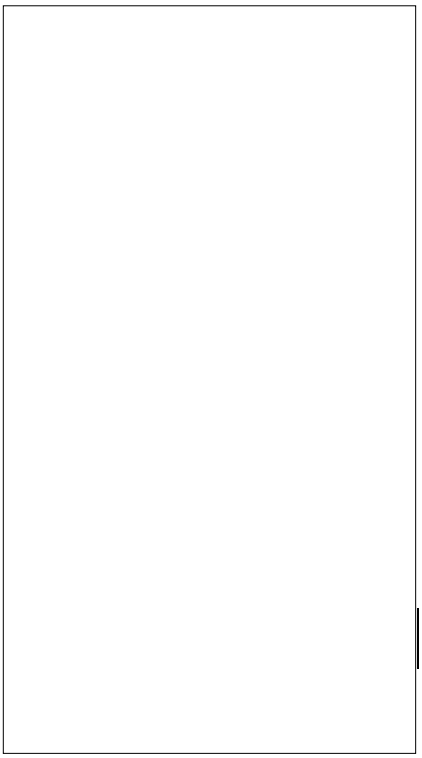
JCT1675Z 75A SCR

Rev.A.1.1

DESCRIPTION:

With high ability to withstand the shock loading of large current, JCT1675Z SCR provides high dV/dt rate with strong resistance to electromagnetic interference.It is especially recommended for use on solid state relay, UPS, SVC, power charger, T-tools etc. JCT1675Z provide a rated insulation voltage of 2500 $V_{RMS,}$ complyingwith UL standards (File ref: E252906).Package TO-3P is RoHS compliant.



MAIN FEATURES

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction tem			

Peak gate current (t_p=20μs , T_j=125)	I_{GM}	12	A
Average gate power dissipation (T_j=125)	$P_{G(AV)}$	1	W
Peak gate power	P_{GM}	22	W

FIG.1: Maximum power dissipation versus RMS on-state current

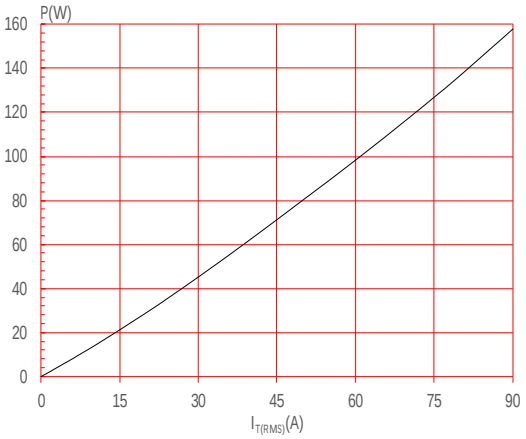


FIG.2: RMS on-state current versus case temperature

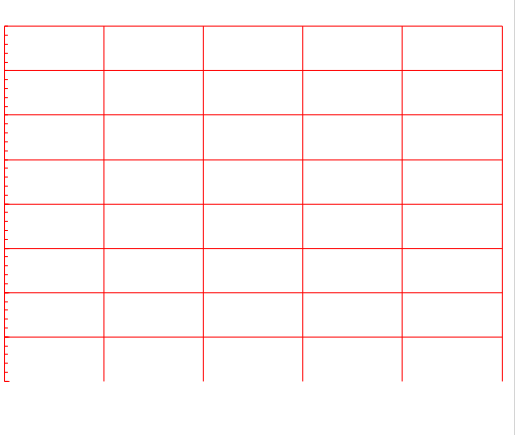
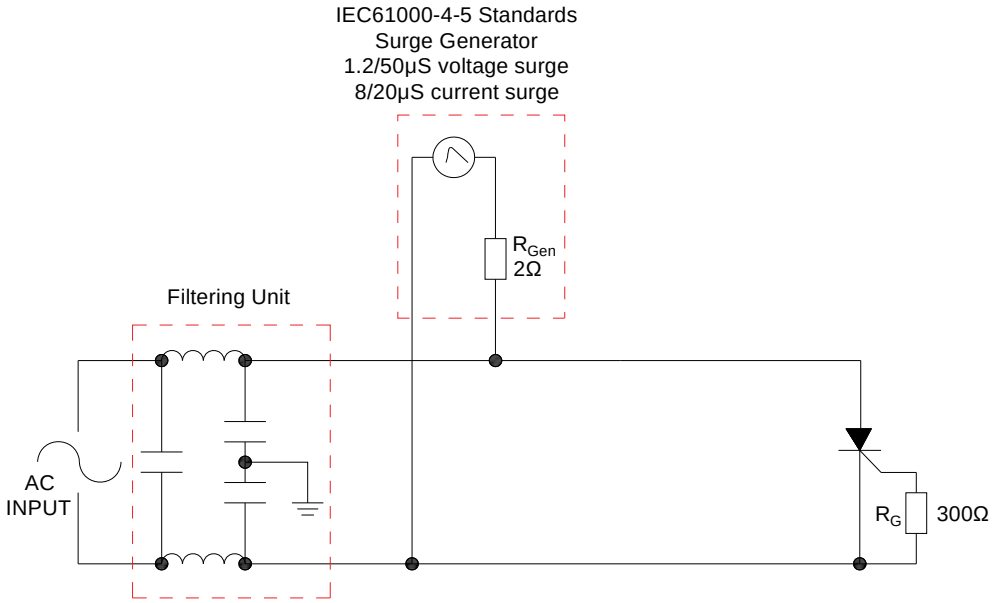


FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards.



LEAD FORMING AND SOLDERING

Refer to the application note “Assembly Instructions for Thyristors in Through-hole Package” released by JieJie Microelectronics

ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
JCT1675Z	1600	10-80	TO-3P	30	Tube

Document Revision History

Date	Revision	Changes
May 7, 2024	A.1.0	Last update
Oct.16, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

PACKAGE MECHANICAL DATA



Information furnished in this document is believed to be accurate and reliable. " 86 "