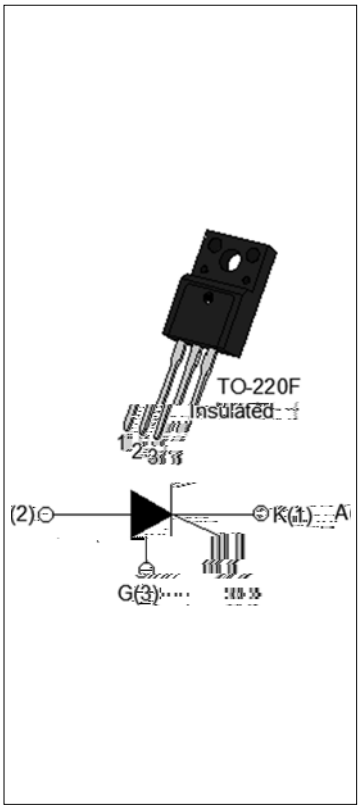
JCT812TF 12A SCR

Rev.A.1.1

DESCRIPTION:

JCT812TF silicon controlled rectifier is specifically designed for medium power switching and phase control applications. High current density due to mesa technology; SIPOS and Glass Passivation technology used has reliable operation up to 125 junction temperature. Low I_{GT} parts available. From all three terminals to external heatsink, JCT812TF provides a rated insulation voltage of 2000 V_{RMS}, complying with UL standards (File ref: E252906). Package TO-220F is RoHS compliant.



MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	12	A
V_{DRM}/V_{RRM}	800	V
I_{GT}	≤5	mA

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage (T_j=25)	V_{DRM}	800	V
Repetitive peak reverse voltage (T_j=25)	V_{RRM}	800	V
Average on-state current (T_C ≤ 86)	$I_{T(AV)}$	7.6	A
RMS on-state current (T_C ≤ 86)	$I_{T(RMS)}$	12	A
Non repetitive surge peak on-state current (t_p=10ms, T_j=25)	I_{TSM}	140	A
Non repetitive surge peak on-state current (t_p=8.3ms, T_j=25)		154	
I^2t value for fusing (t_p=10ms , T_j=25)	I^2t	98	A^2s
Critical rate of rise of on-state current (I_G=2 × I_{GT}, f=100Hz , T_j=125)	dI/dt	100	A/μs

Peak gate current (t_p=20μs , T_j=125)	I_{GM}	4	A
Average gate power dissipation (T_j=125)	$P_{G(AV)}$	1	W
Peak gate power	P_{GM}	10	W
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	0.5	kV

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

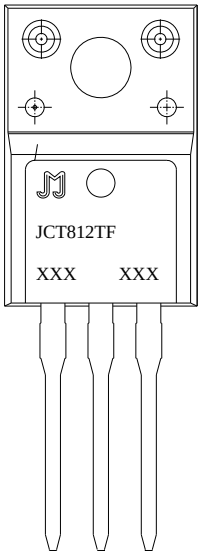
Symbol	Test Condition	Value			Unit
		MIN.	TYP.	MAX.	
I_{GT}	V_D=12V R_L=33Ω	-	-	5	mA
V_{GT}		-	-	1	V
V_{GD}	V_D=V_{DRM} T_j=125 R_L=3.3kΩ	0.2	-	-	V
I_L	I_G=1.2I_{GT}	-	-	30	mA
		-	-	15	mA
	25	200	-	-	V/μs
	mA	-	5	-	μs
		-	80	-	

er	Value(MAX.)	Unit
T_j=25	1.5	V
T_j=125	0.8	V
T_j=125 @	27	mΩ
T_j=25	5	μA

ORDERING INFORMATION

J	CT	8	12	T	F
JieJie Microelectronics Co.,Ltd.	SCRs	8:V_{DRM} /V_{RRM} ≥800V	$I_{T(RMS)}$:12A	T:I_{GT}≤5mA	F:TO-220F(Ins)

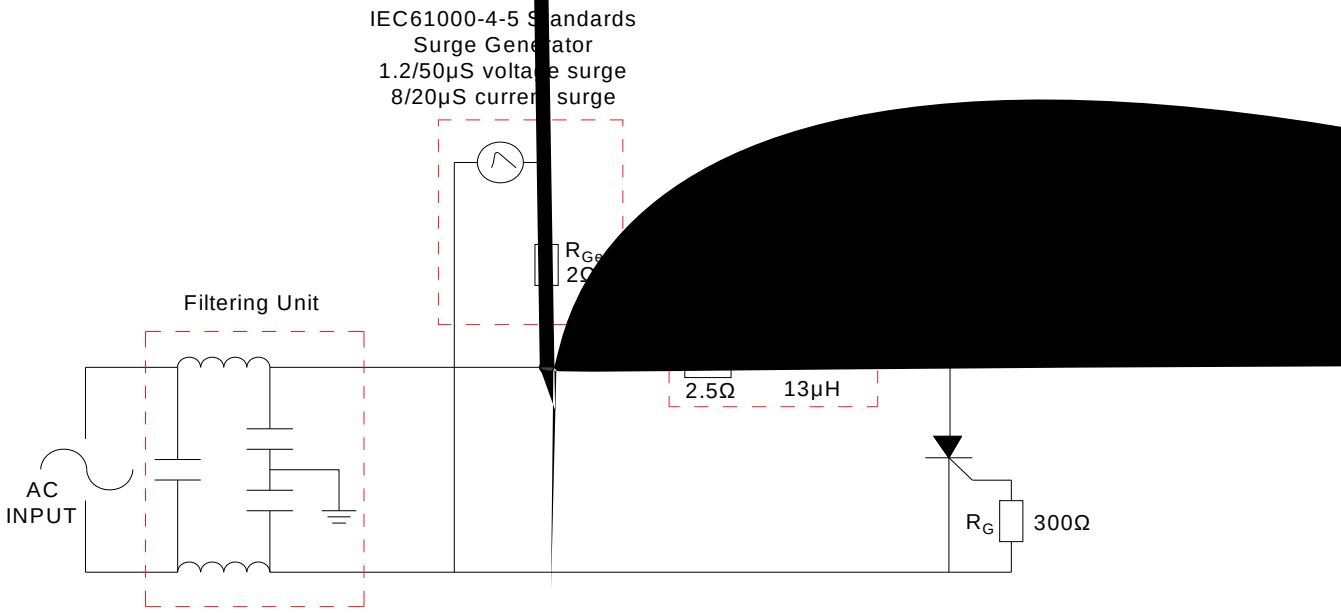
MARKING



XXX XXX

FIG.1: Maximum power dissipation versus

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards.



ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
JCT812TF	800	5	TO-220F(Ins)	50	Tube

Document Revision History

Date	Revision	Changes
Apr.13, 2023	A.1.0	Last update
Sept.29, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

PACKAGE MECHANICAL DATA



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