| | | | |
|---|---|---|---|
| Peak gate power | $P_{GM}$ | 20 | W |

## ORDERING INFORMATION

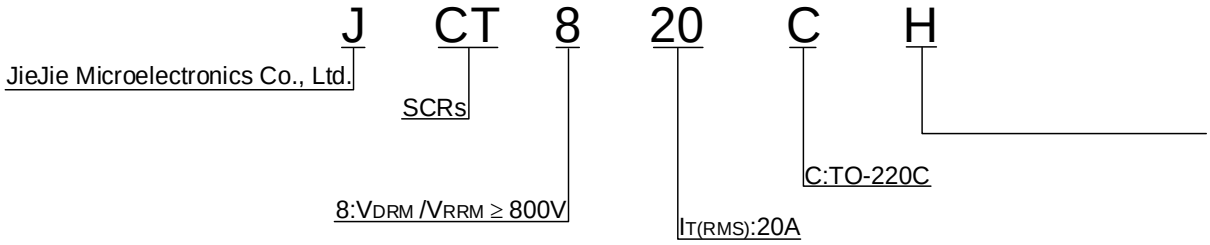


FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-

## ORDERING INFORMATION

| Order code | Voltage $V_{DRM}/V_{RRM}$ (V) | IGT( |
|---|---|---|

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