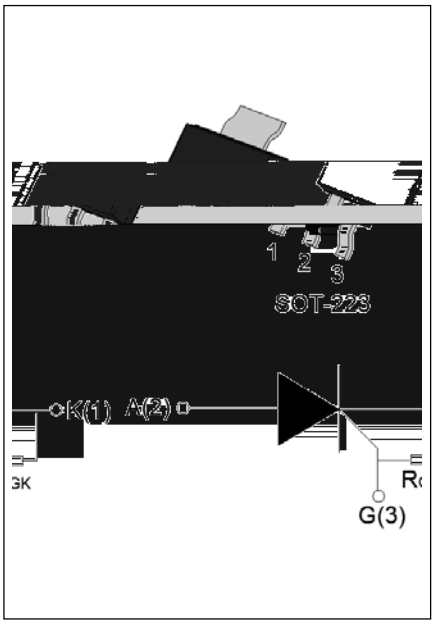
JR0205V 2A SCR

Rev.A.1.1

DESCRIPTION:

The JR0205V SCR with the parallel resistor between Gate and Cathode, R_{GK}=10~80kΩ is especially recommended for use on straight hair, igniter, anion generator, etc. Package SOT-223 is RoHS compliant.



MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	2	A
V_{DRM}/V_{RRM}	600	V
I_{GT}	≤200	μA

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage (T_j=25)	V_{DRM}	600	V
Repetitive peak reverse voltage (T_j=25)	V_{RRM}	600	V
Average on-state current (T_C 72)	$I_{T(AV)}$	1.3	A
RMS on-state current (T_C 72)	$I_{T(RMS)}$	2	A
Non repetitive surge peak on-state current (t_p=10ms , T_j=25)	I_{TSM}	20	A
Non repetitive surge peak on-state current (t_p=8.3ms , T_j=25)		22	
I^2t value for fusing (t_p=10ms , T_j=25)	I^2t	2	A^2s
Critical rate of rise of on-state current (I_G=2× I_{GT} , f=100Hz , T_j=125)	dI/dt	50	A/μs
Peak gate current (t_p=20μs, T_j=125)	I_{GM}	1.2	A
Average gate power dissipation (T_j=125)	$P_{G(AV)}$	0.2	W

Peak gate power	P_{GM}	2	W

Peak pulse voltage
(T_j=25 ; non-repetitive,off-

FIG.1: Maximum power dissipation versus RMS on-state current

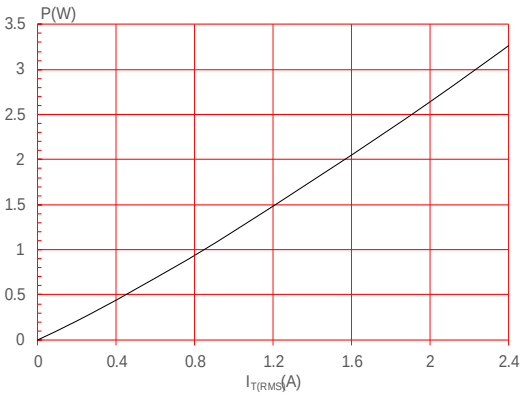


FIG.2: RMS on-state current versus case temperature

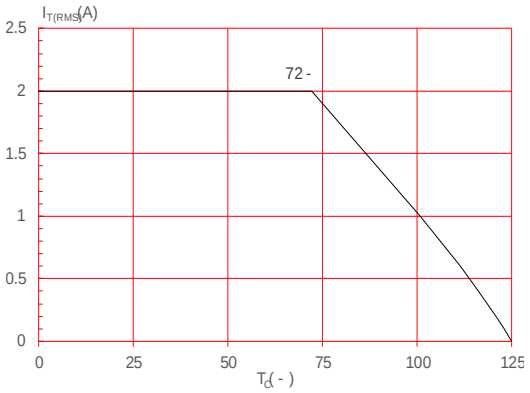
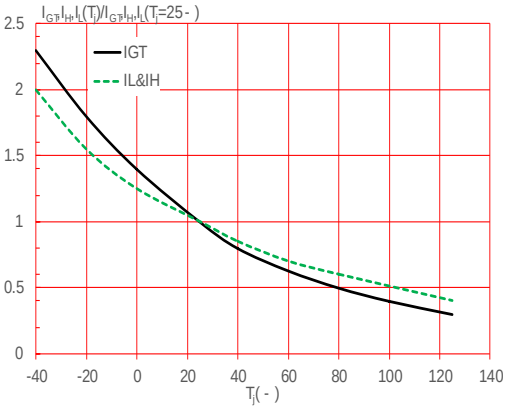


FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

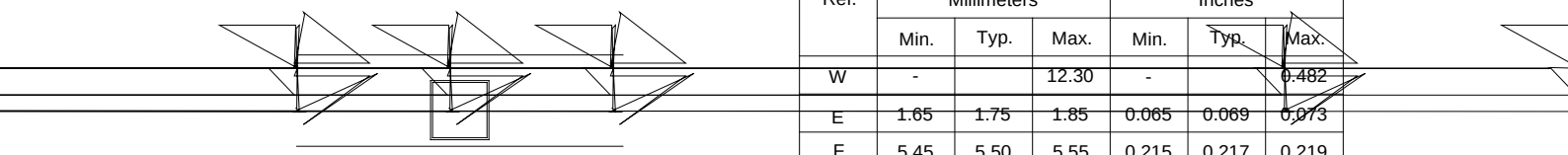


JR0205V

ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT	Package	Base qty. (pcs)	Delivery mode

DELIVERY MODE



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
W	-		12.30	-		0.482
E	1.65	1.75	1.85	0.065	0.069	0.073
F	5.45	5.50	5.55	0.215	0.217	0.219
D0		1.55	1.60		0.061	0.063
D1		-	-			
P0	3.90	4.00	4.10	0.154	0.157	0.161
P1	7.90	8.00	8.10	0.311	0.315	0.319
P2	1.95	2.00	2.05	0.077	0.079	0.081
10P0	39.80	40.00	40.20	1.567	1.575	1.583
A0	6.85	6.95	7.05	0.269	0.273	0.276
B0	7.15	7.25	7.35	0.280	0.284	0.288
K0	1.95	2.05	2.15	0.076	0.080	0.084
T	0.20	0.25	0.30	0.008	0.010	0.012

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