## JX008L 0.8A Sensitive SCR

Rev.A.1.0

### DESCRIPTION:

The JX008L SCR provides high dV/dt rate with strong resistance to electromagnetic interface. It is especially recommended for use on residual current circuit breaker, straight hair, igniter etc. Package SOT-23-3L is RoHS compliant.

### MAIN FEATURES

| Symbol | Value | Unit |
|---|---|---|
| $I_{T(RMS)}$ | 0.8 | A |
| $V_{DRM}$ /$V_{RRM}$ | 800 | V |
| $I_{GT}$ | ≤200 | μA |

### ABSOLUTE MAXIMUM RATINGS

| Parameter | Symbol | Value | Unit 9o 0.48Tj 0.4 |
|---|---|---|---|

| | | | |
|---|---|---|---|
| Peak gate power | $P_{GM}$ | 2 | W |
| Peak pulse voltage ($T_j$=25 ; non-repetitive,off-state;FIG.8) | $V_{pp}$ | 1 | kV |

**NOTE 1:** Operating junction temperature $T_j$ is up to 125 when a resistor ≤1kΩ is connected between Gate and Cathode. Without this resistor, the $T_j$ is up to 110 only.

## ELECTRICAL CHARACTERISTICS ($T_j$=25 unless otherwise specified)

| Symbol | Test Condition | Value | | | Unit |
|---|---|---|---|---|---|
| | | MIN. | TYP. | MAX. | |
| $I_{GT}$ | $V_D$=12V $R_L$=33Ω | - | 50 | | |

**FIG.1:** Maximum power dissipation versus RMS on-state current



**FIG.2:** RMS on-state current versus case temperature

**FIG.7:** Relative variations of gate trigger current, holding current and latching current versus junction temperature

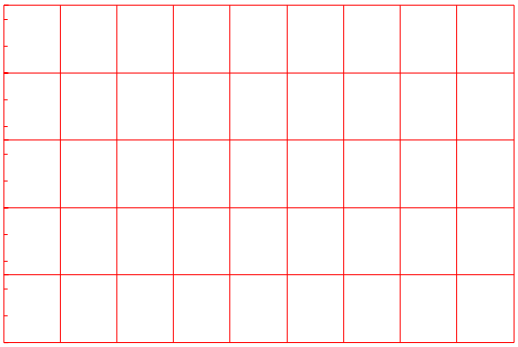


FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards.



## ORDERING INFORMATION

| Order code | Voltage $V_{DRM}/V_{RRM}$ (V) | IGT(μA) | Package | Base qty. (pcs) | Delivery mode |
|---|---|---|---|---|---|
| JX008L | 800 | ≤200 | SOT-23-3L | 3,000 | Tape & Reel |

**JX008L** **JieJie M**