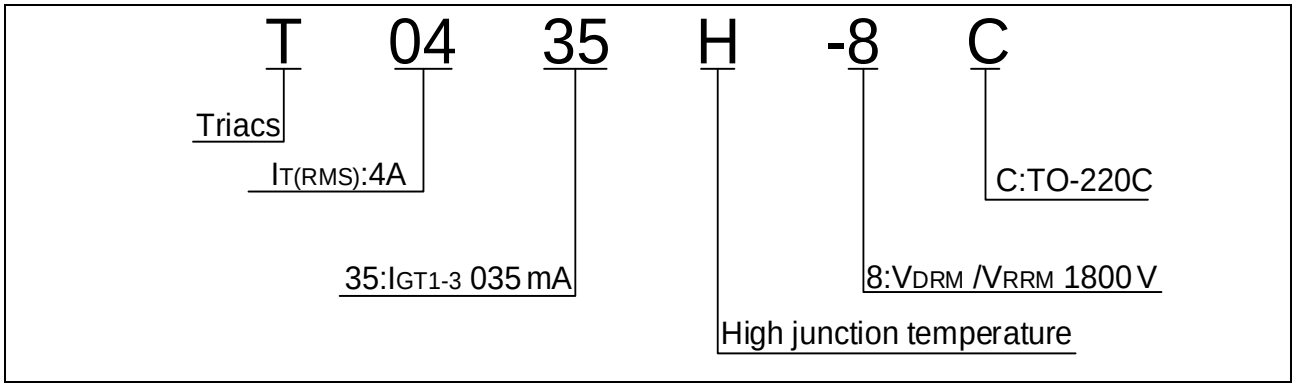
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	4	kV

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D =12V R_L =33	- -	MAX.	35	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	V_D =V_{DRM} T_j =150				

ORDERING INFORMATION



MARKING

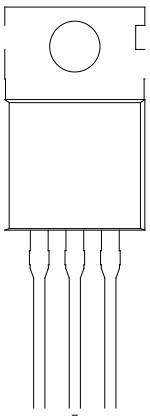
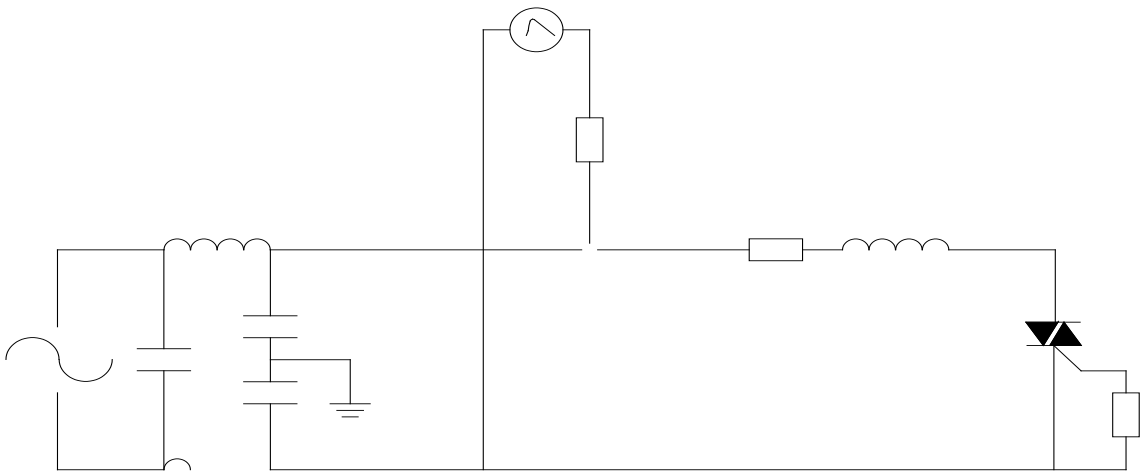


FIG.1: Maximum power dissipation versus RMS on-state current

FIG.2: RMS on-state current versus case temperature t @ã !2Hv3R #7THIqi x°sIÀ p3 1gRXcu) C

FIG.7 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards RRRRRRRRR



ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
T0435H-8C	800	35	TO-220C	50	Tube

Document Revision History

Date	Revision	Changes
Apr.10, 2023	A.1.0	Last updated
Oct.14, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

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