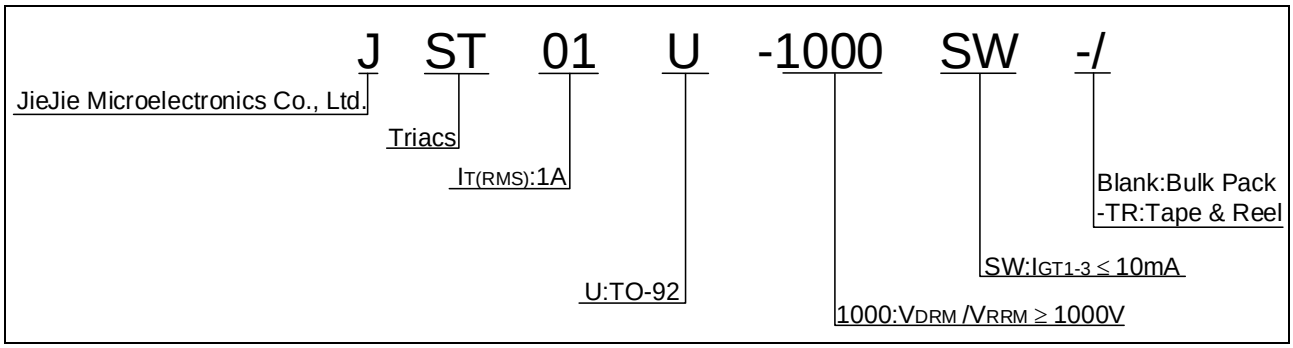
TEL

Peak gate power	P_{GM}	2	W
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	4	kV


J ST 01 U -1000 SW -/
JieJie Microelectronics Co., Ltd.
Triacs
IT(RMS):1A
U:TO-92
1000:VDRM /VRRM ≥ 1000V
SW:IGT1-3 ≤ 10mA
Blank:Bulk Pack
-TR:Tape & Reel

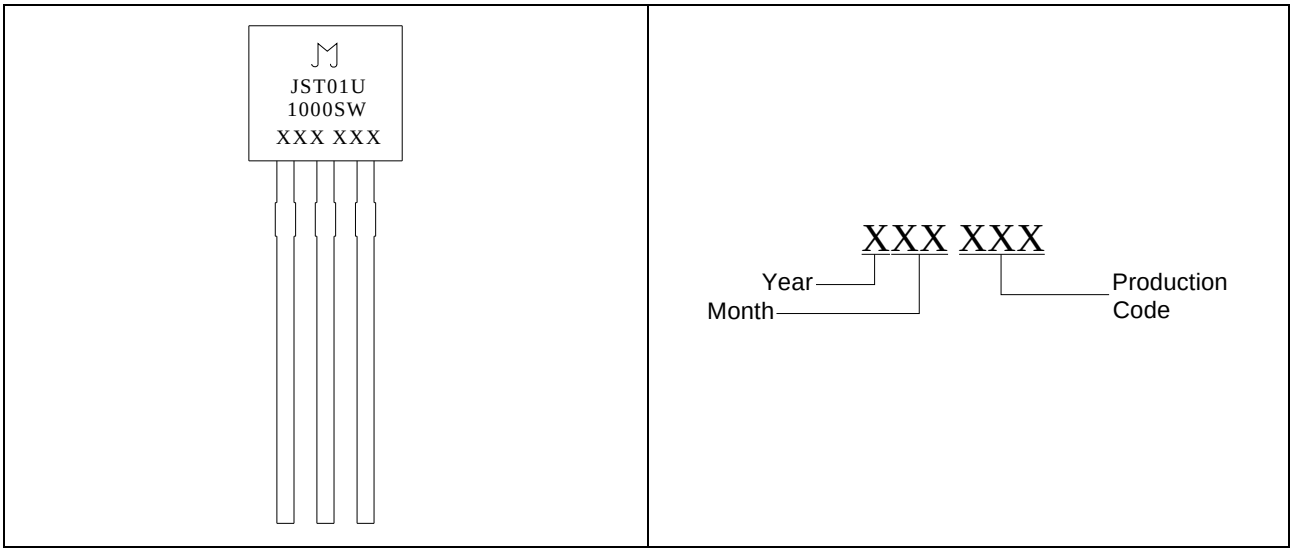
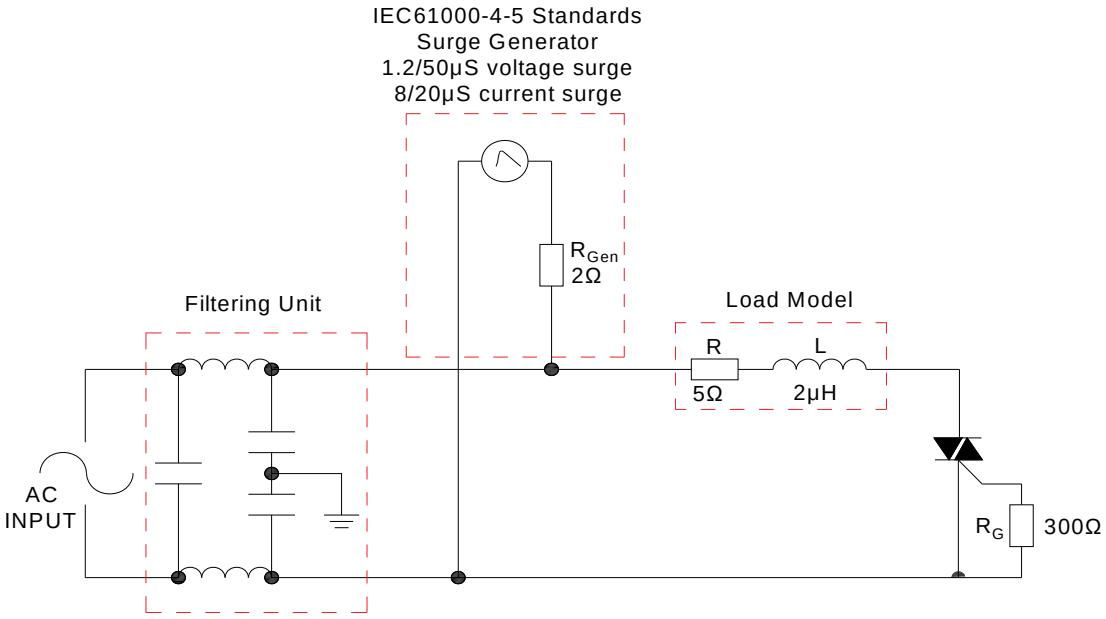
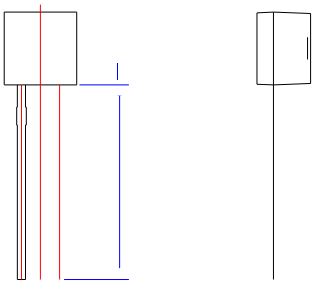

JST01U
1000SW
XXX XXX
XXX XXX
Year
Month
Production
Code

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



Date	Revision	Changes
Apr.10, 2023	A.1.0	Last updated
Apr.3, 2025	A.2.0	Renew PACKAGE MECHANICAL DATA
Sept.28, 2025	A.2.1	Revise PACKAGE MECHANICAL DATA





TO-92	Bulk Pack	1,000	10,000	50,000

W	17.50	18.00	19.00	6.00 6.00
W0	5.50	6.00	6.50	
W1	8.50	9.00	9.50	
W2			1.00	
D0	3.80	4.00	4.20	
H	-1.00	0	1.00	
L1	2.50			
H	18.00	19.00	20.00	

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