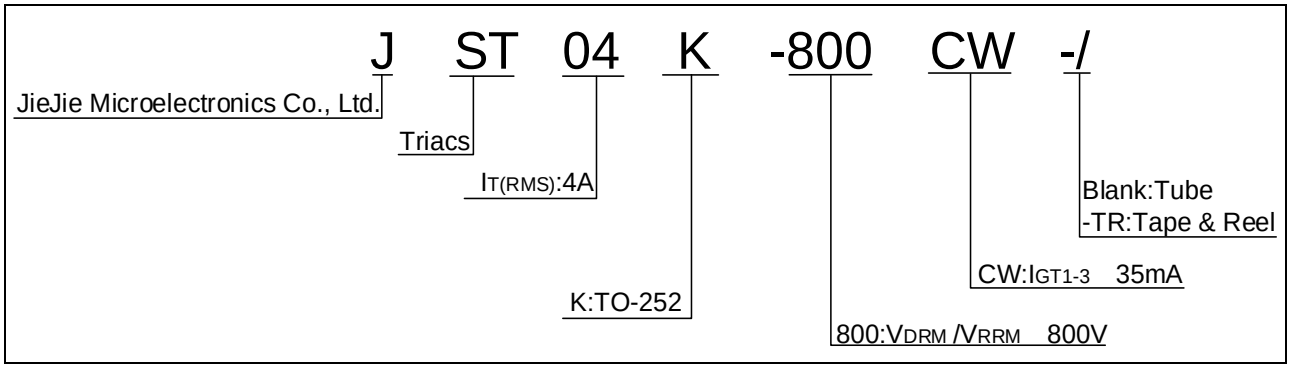
JST04K-800CW

ORDERING INFORMATION



MARKING

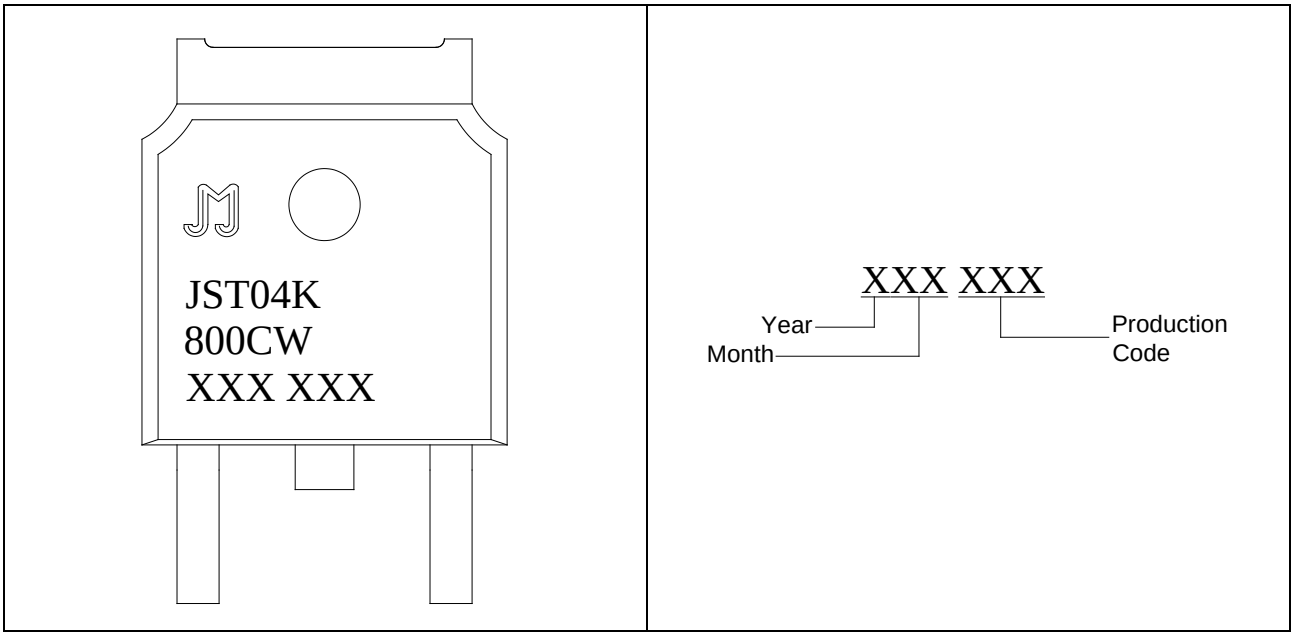


FIG.1: Maximum power dissipation versus RMS on-state current

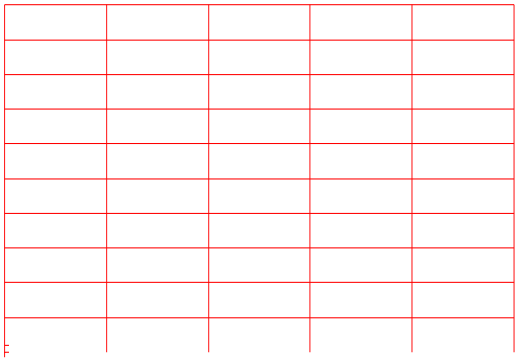
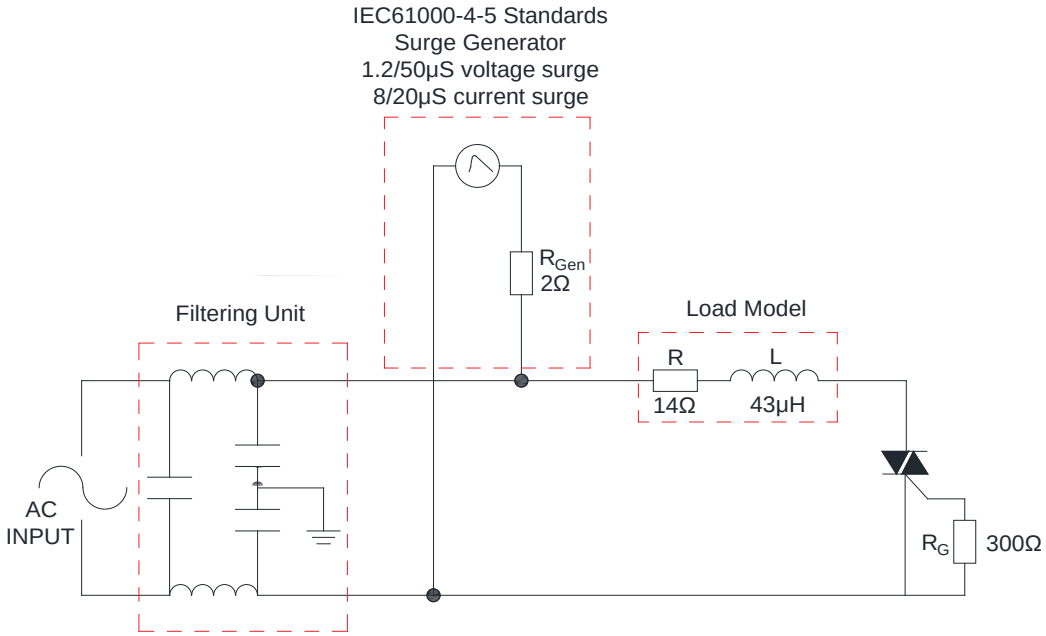


FIG.2: RMS on-state current versus case temperature012345

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards

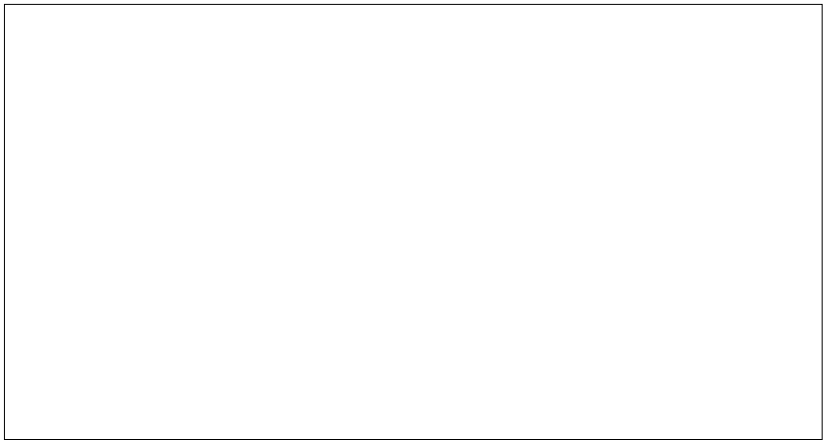


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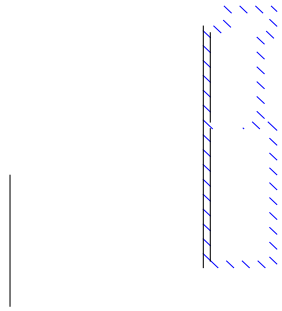
Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode

PACKAGE MECHANICAL DATA

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.15	0		0.006
B	0.66		0.86	0.026		0.034
C	0.40		0.60	0.016		0.024
D	5.90		6.30			
E	6.40					
G						
G1	2.18		2.38	0.086		0.094
L						
L200				0.053		0.065



DELIVERY MODE (PCS)INNER



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