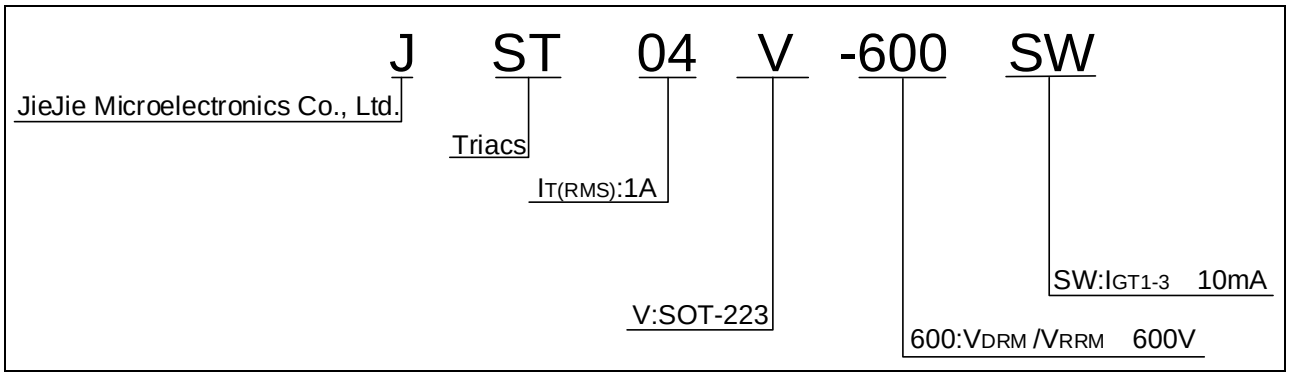
JST04V-600SW 1A TRIAC

Peak pulse voltage
(T_j=25 ; non-rept66Qioltvt

ORDERING INFORMATION



MARKING

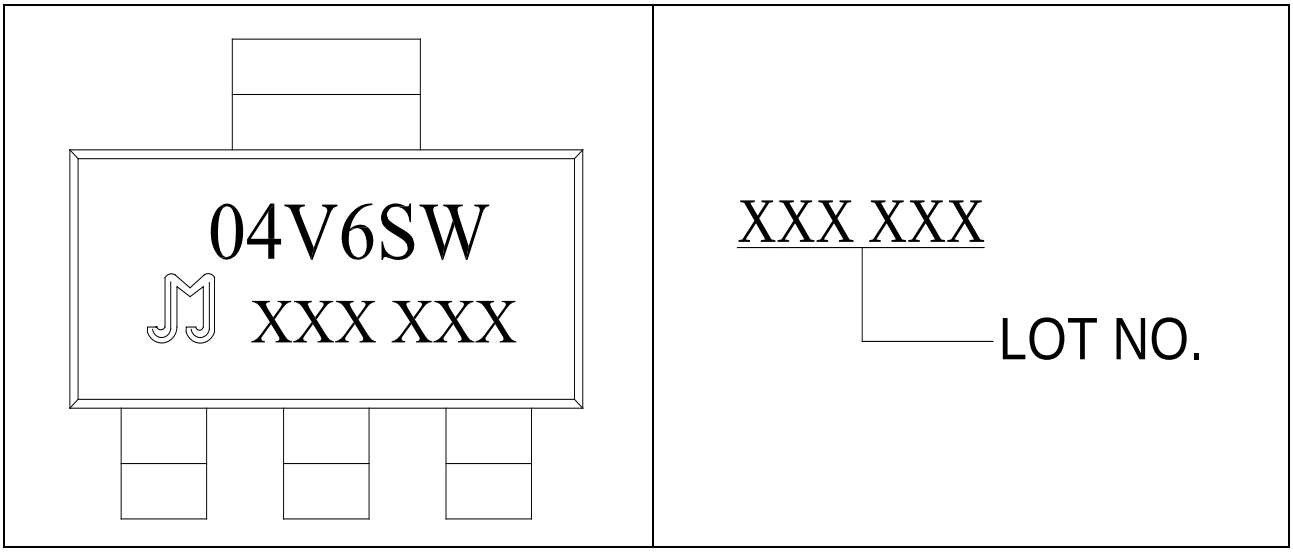


FIG.1: Maximum power dissipation versus RMS on-state current

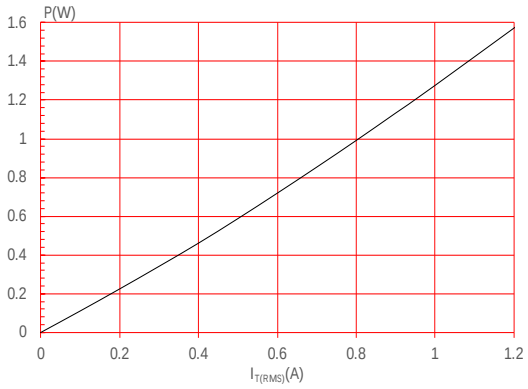


FIG.2: RMS on-state current versus case temperature

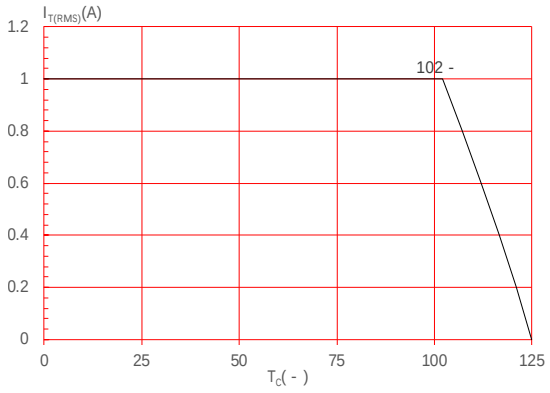


FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

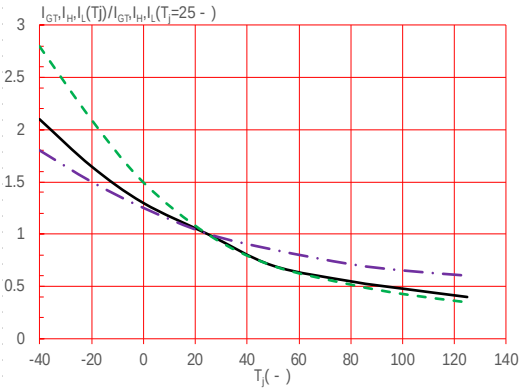
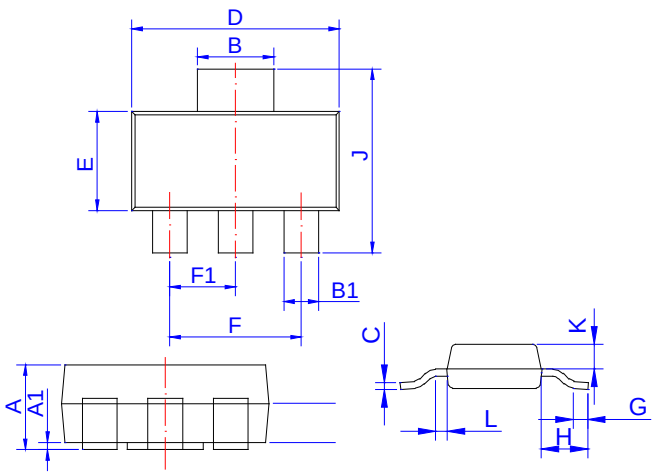


FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards

PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	1.50	1.60	1.80	0.059	0.063	0.071
A1	0.01	0.06	0.10	0.001	0.002	0.004
B	2.90	3.00	3.10	0.114	0.118	0.122
B1	0.60	0.70	0.80	0.024	0.028	0.031
C	0.22	0.26	0.32	0.009	0.010	0.013
D	6.30	6.50	6.70	0.248	0.256	0.264
E	3.30	3.50	3.70	0.130	0.138	0.146
F	4.40		4.80	0.173		0.189
F1	2.20		2.40	0.087		0.094
G	0.50		1.00	0.020		0.039
H	1.50	1.75	2.00	0.059	0.069	0.079
J	6.70	7.00	7.30	0.264	0.276	0.287
K	0.80		1.00	0.031		0.039
L	0.40		0.80	0.016		0.031
M			0.80	0.026		0.031

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