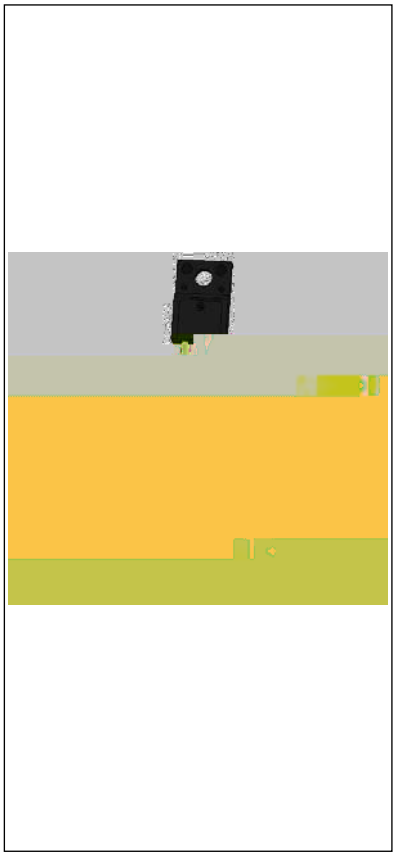
JST08F-1200CW 8A TRIAC

Rev.A.1.1

DESCRIPTION:

The JST08F-1200CW triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. JST08F-1200CW snubberless triac is especially recommended for use on inductive loads. By using an external plastic package, JST08F-1200CW provides a rated insulation voltage of 2000 VRMS, complying with UL standards (File ref: E252906). Package TO-220F is RoHS compliant.



MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	8	A
V_{DRM} /V_{RRM}	1200	V
I_{GT} / /	35/35/35	mA

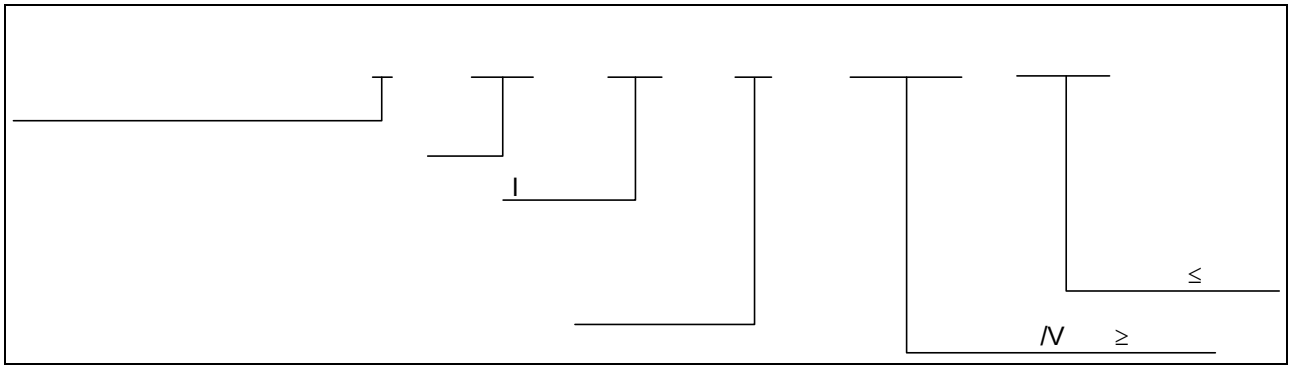
ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage (T_j=25)	V_{DRM}	1200	V
Repetitive peak reverse voltage (T_j=25)	V_{RRM}	1200	V
RMS on-state current ($T_C \leq$ 94)	$I_{T(RMS)}$	8	A
Non repetitive surge peak on-state current (full cycle , t_p=20ms , T_j=25)	I_{TSM}	80	A
Non repetitive surge peak on-state current (full cycle , t_p=16.6ms , T_j=25)		88	
I^2t value for fusing (t_p=10ms , T_j=25)	I^2t	32	A^2s
Critical rate of rise of on-state current (I_G=2 × I_{GT} , f=100Hz , T_j=125)	dI/dt	100	A/μs
Peak gate current (t_p=20μs , T_j=125)	I_{GM}	4	A

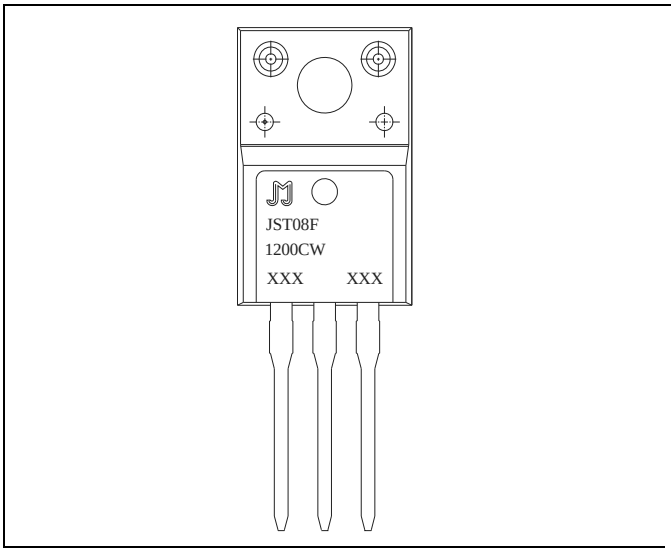
Average gate power dissipation (T_j=125)	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	10	W
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	2	kV

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
~~I_{GT}~~	V_D =12V R_L =33Ω	- -	MAX.	35	mA
V_{GT}		- -	MAX.	1	V



MARKING



XXX XXX

Year
Month

Production
Code

FIG.1: Maximum power dissipation versus RMS on-state current

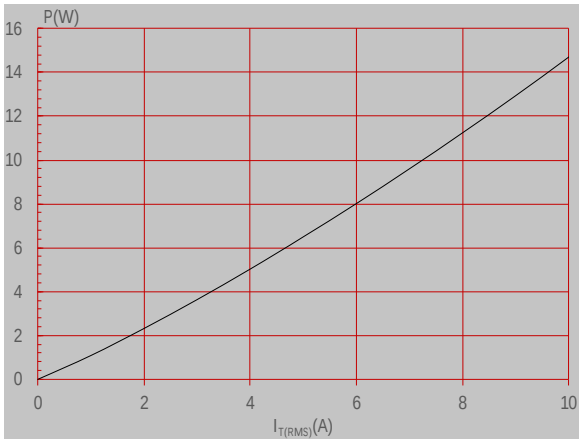


FIG.2: RMS on-state current versus case temperature

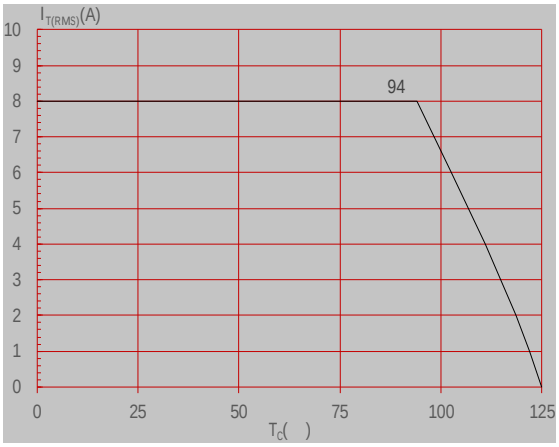


FIG.3: Surge peak on-state current versus number of cycles

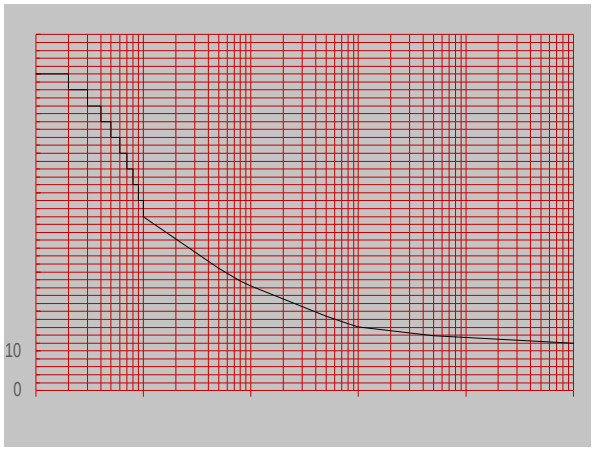
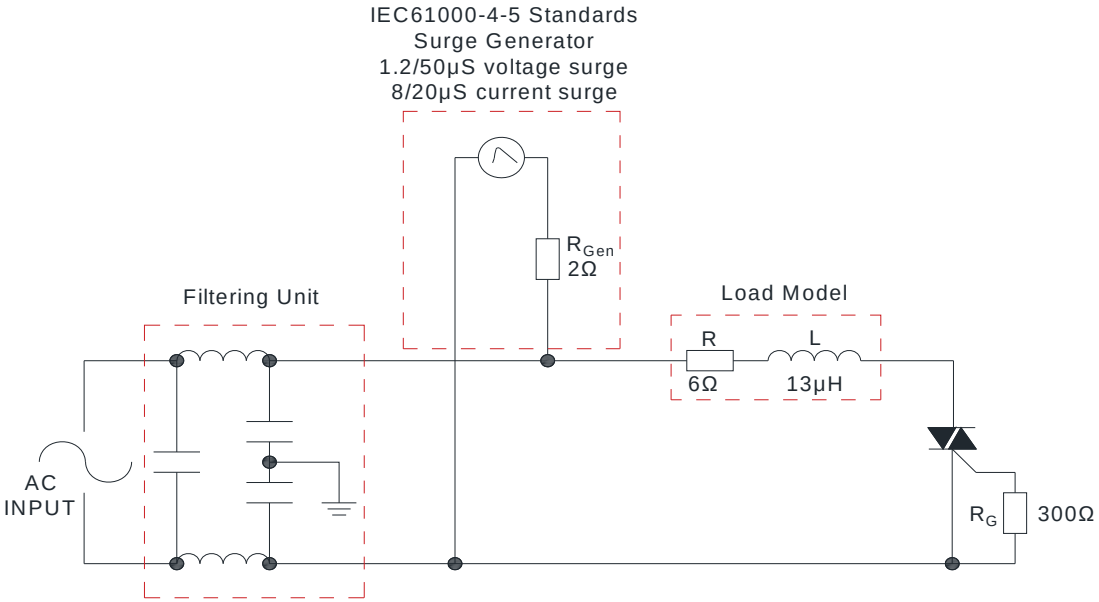


FIG.4: On-state characteristics

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



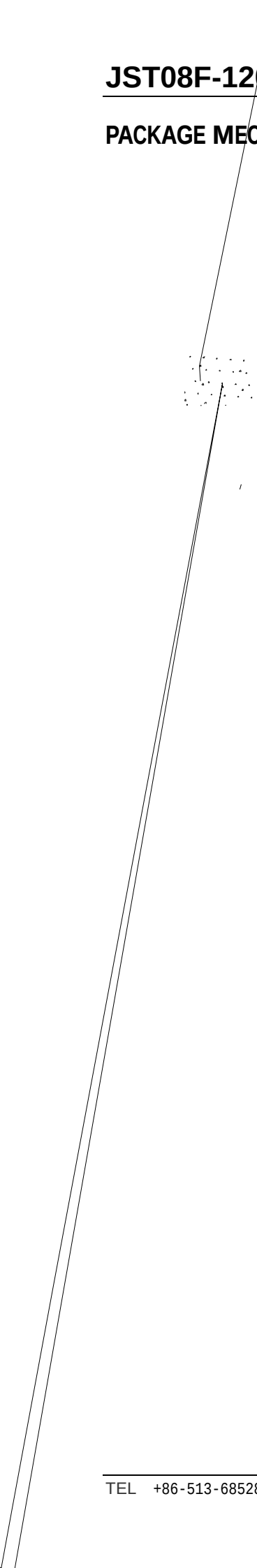
LEAD FORMING AND SOLDERING

Refer to the application note “Assembly Instructions for Thyristors in Through-hole Package” released by JieJie Microelectronics.

ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
JST08F-1200CW	1200	35	TO-220F(Ins)	50	Tube

PACKAGE MECHANICAL DATA



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