Peak gate current (t_p=20εs , T_j=125) I >>BDC Tw 12 020 0 8.04 222.36 76328.3Tm (p)

ORDERING INFORMATION

J ST 100 IS -1200 BW

- J: JieJie Microelectronics Co., Ltd.
- ST: Triacs
- 100: $I_{T(RMS)}$:100A
- IS: ITO-247(Ins)
- -1200: 1200:V_{DRM} /V_{RRM} ≥ 1200V
- BW: I_{GT1-3} ≤ 50mA

MARKING

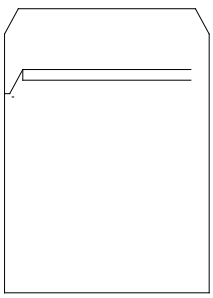


FIG.1: Maximum power dissipation versus RMS on-state current

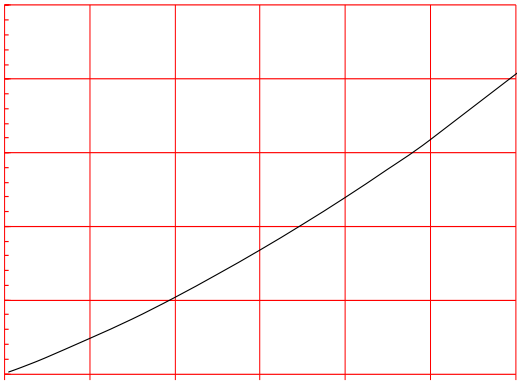


FIG.2: RMS on-state current versus case temperature

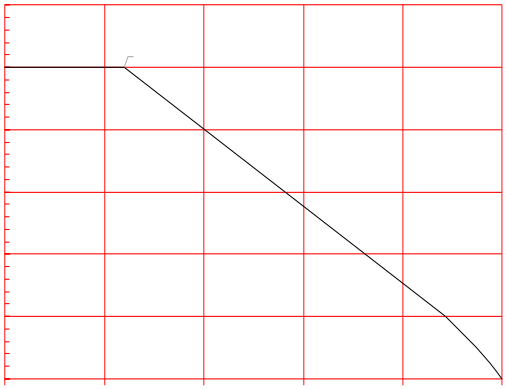


FIG.3: Surge peak on-state current versus number of cycles

FIG.4: On-state characteristic

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



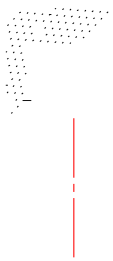
ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM}(V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
JST100IS-1200BW	1200	50	ITO-247(Ins)	25	Tube

Document Revision History

Date	Revision	Changes
Apr.11, 2023	A.1.0	Last updated
Oct.16, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

PACKAGE MECHANICAL DATA



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