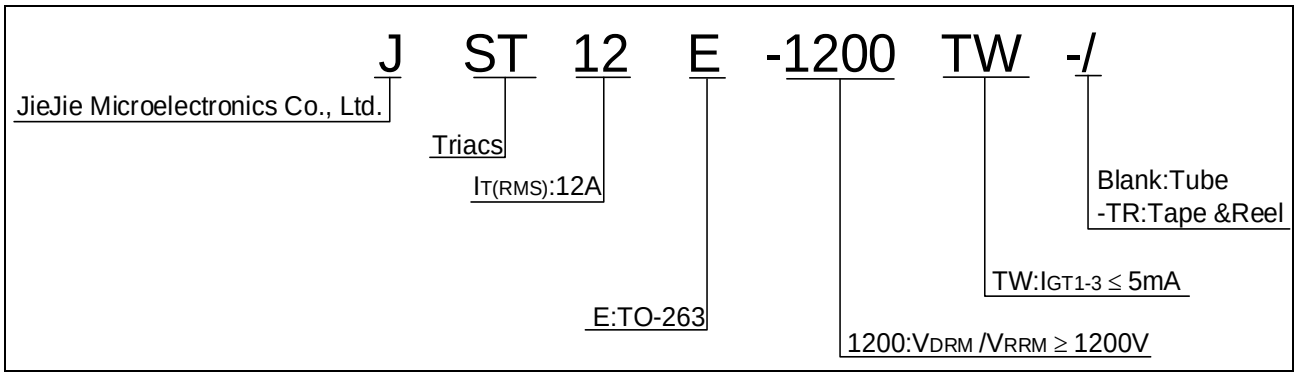
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.8)	V_{pp}	3.5	kV

(T_j=25 unless otherwise specified)

Symbol Test Condition Quadrant


J ST 12 E -1200 TW -/
JieJie Microelectronics Co., Ltd.
Triacs
IT(RMS):12A
E:TO-263
1200:VDRM /VRRM ≥ 1200V
TW:IGT1-3 ≤ 5mA
Blank:Tube
-TR:Tape &Reel

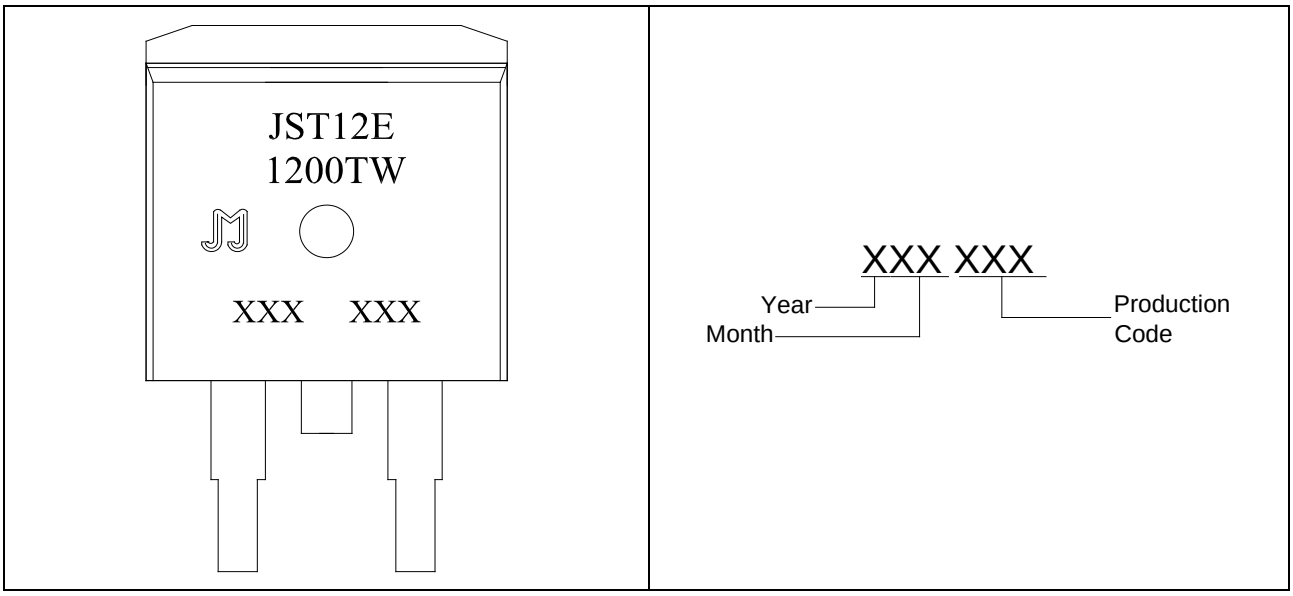

JST12E
1200TW
XXX XXX
XXX XXX
Year
Month
Production
Code

FIG.1: Maximum power dissipation versus RMS on-state current

FIG.2: RMS on-state current versus case temperature

FIG.7:

FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards

