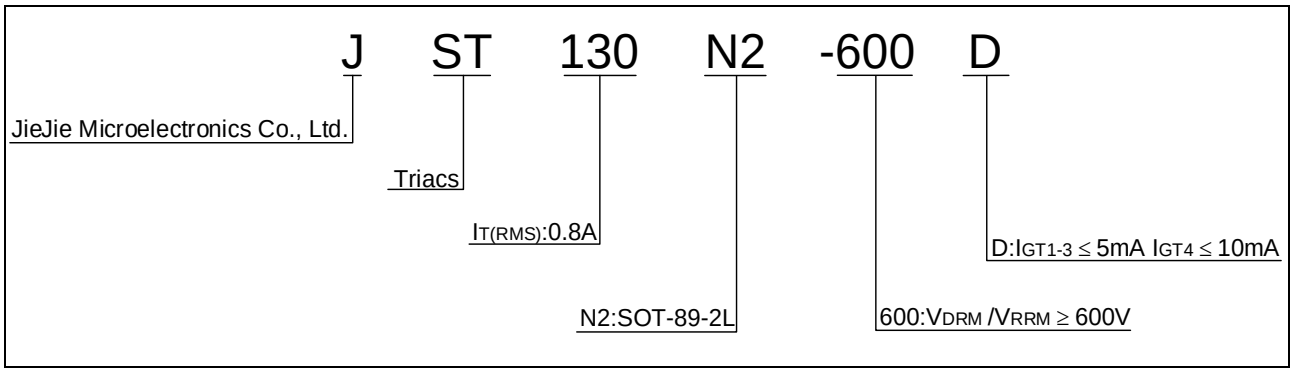
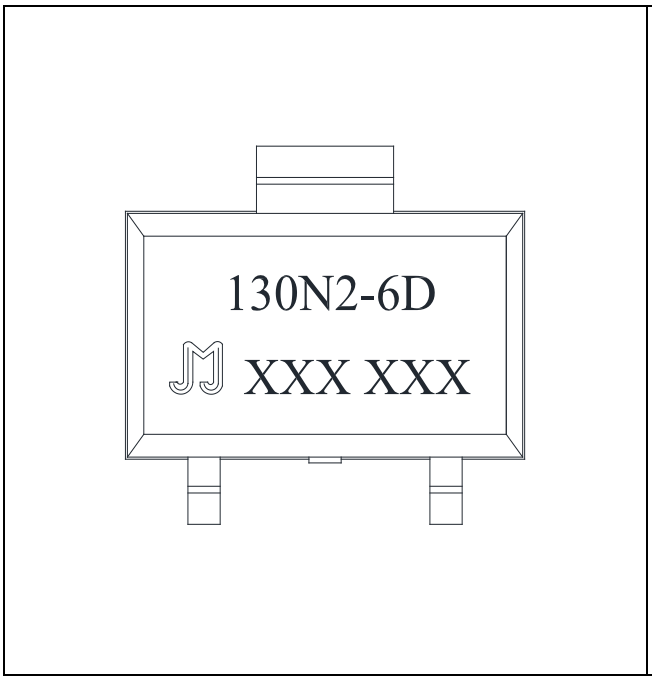
(T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D=12V R_L=33	- -	MAX.	5	mA
				10	
V_{GT}		ALL	MAX.	1.3	V
V_{GD}	V_D=V				


J
ST
130
N2
-600
D
JieJie Microelectronics Co., Ltd.
Triacs
IT(RMS):0.8A
N2:SOT-89-2L
600:VDRM /VRRM ≥ 600V
D:IGT1-3 ≤ 5mA IGT4 ≤ 10mA


130N2-6D
XXX XXX

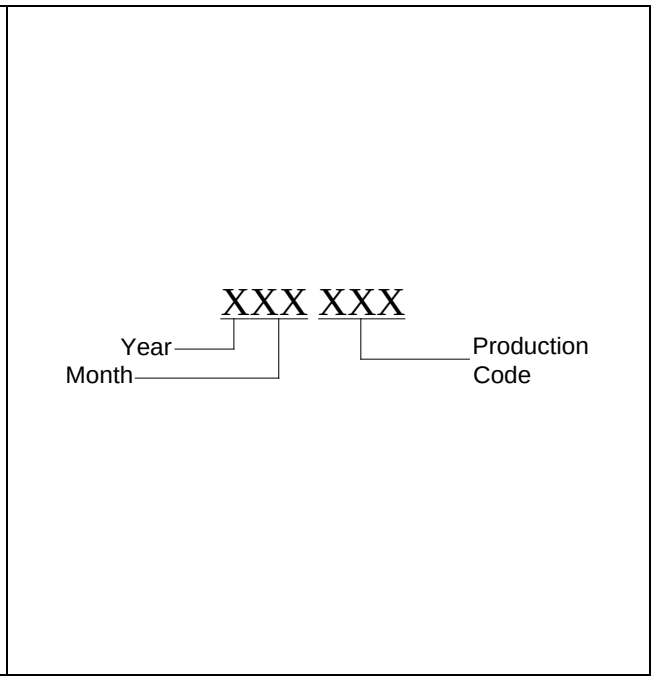

XXX XXX
Year
Month
Production
Code

FIG.1: Maximum power dissipation versus RMS on-state current

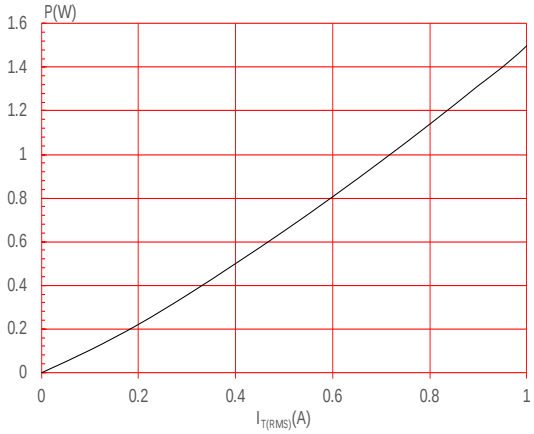


FIG.2: RMS on-state current versus case temperature

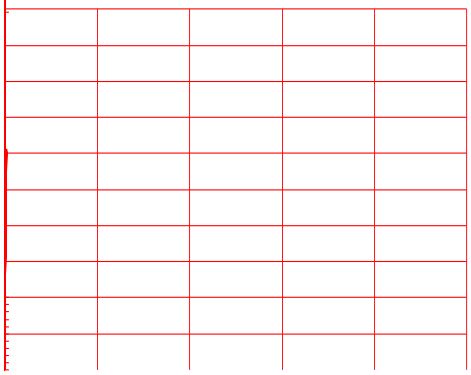
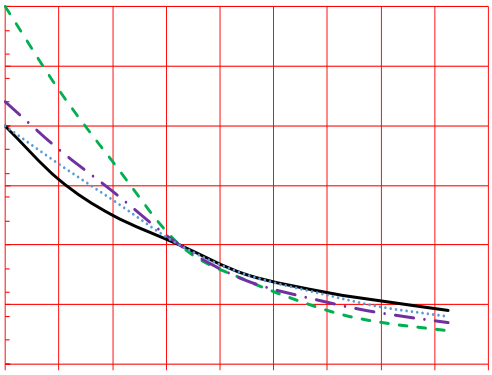


FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature



Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		H- I- J	K			
JST130N2-600D	**600**	**5**	**10**	**SOT-89-2L**	**4,000**	**Tape & Reel**

Document Revision History

Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated
Oct.16, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA