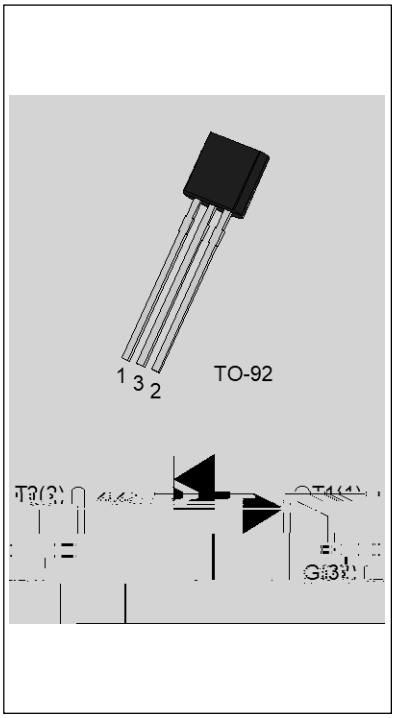
JST134U-800CW 2A TRIAC

Rev.A.2.1

DESCRIPTION:

The JST134U-800CW triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. JST134U-800CW snubberless triac is especially recommended for use on inductive loads. Package TO-92 is RoHS compliant.



MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	2	A
V_{DRM} /V_{RRM}	800	V
I_{GT} / /	35/35/35	mA

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage (T_j=25)	V_{DRM}	800	V
Repetitive peak reverse voltage (T_j=25)	V_{RRM}	800	V
RMS on-state current	$I_{T(RMS)}$	2	A
Non repetitive surge peak on-state current (full cycle , t_p=20ms , T_j=25)	I_{TSM}	27	A
Non repetitive surge peak on-state current (full cycle , t_p=16.6ms , T_j=25)		30	
I^2t value for fusing (t_p=10ms , T_j=25)	I^2t	3.7	A^2s
Critical rate of rise of on-state current (I_G=2 I_{GT} , f=100Hz , T_j=125)	dI/dt	80	A/μs
Peak gate current (t_p=20μs , T_j=125)	I_{GM}	2	A
Average gate power dissipation (T_j=125)	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	5	W

state;FIG.7)		V_{pp}	3	kV

TICS (T_j=25 unless otherwise specified)

on	Quadrant		Value	Unit

JST134U-800CW

FIG.1: Maximum power dissipation versus RMS on-state current

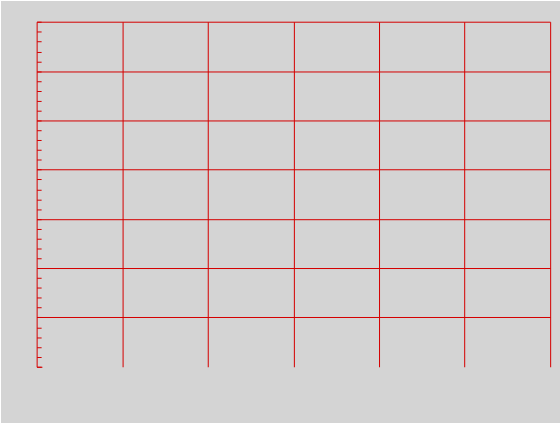


FIG.2: RMS on-state current versus case temperature

JST134U-800CW

ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
JST134U-800CW	800	35	TO-92	1,000	Bulk Pack
JST134U-800CW-TR				2,000	Tape & Reel

Document Revision History

Date	Revision	Changes
Apr.11, 2023	A.1.0	Last updated
Mar.28, 2025	A.2.0	Renew PACKAGE MECHANICAL DATA
Sept.28, 2025	A.2.1	Revise PACKAGE MECHANICAL DATA

DELIVERY MODE

PACKAGE	OUTLINE	BAG (PCS)	INNER BOX (PCS)	CARTON BOX (PCS)
TO-92	Bulk Pack	1,000	10,000	50,000

P	12.40	12.70	13.00W
P0	12.40	12.70	13.00
P1	3.55	3.85	4.15
P2	5.95	6.35	6.75
P	-1.00	0	1.00
F1 F2	2.30	2.50	2.70
F1-F2	-0.10	0	0.10
W	17.50	18.00	19.00
W0	5.50	6.00	6.50
W1	8.50	9.00	9.50
W2			1.00
D0	3.80	4.00	4.20
H	-1.00	0	1.00
L1	2.50		
H	18.00	19.00	20.00

Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document