JST136F-800D 4A TRIAC

Rev.A.1.1

DESCRIPTION:

The JST136F-800D triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. By using an external plastic package, JST136F-800D provides a rated insulation voltage of 2000 VRMS, complying with UL standards (File ref: E252906). Package TO-220F is RoHS compliant.

MAIN FEATURES

ABSOLUTE MAXIMUM RATINGS

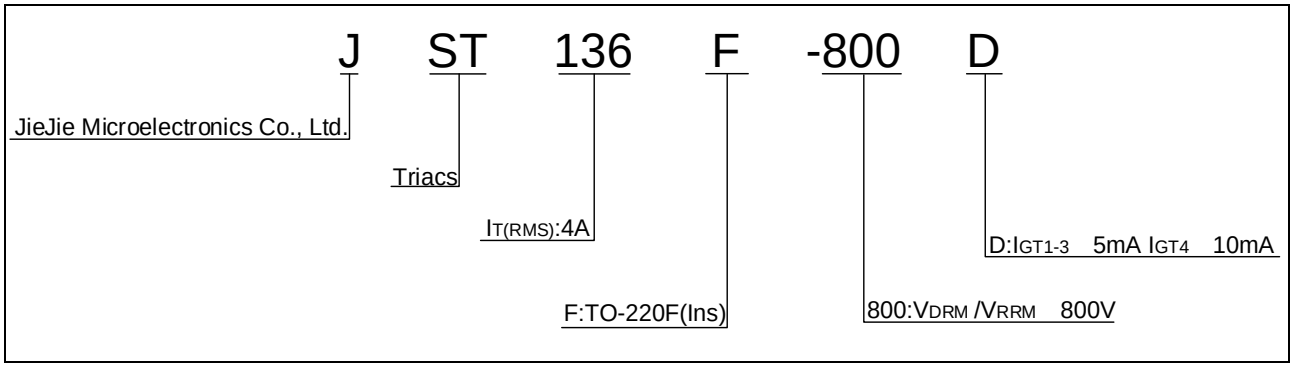
Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage (T_j=25)	V_{DRM}	800	V
Repetitive peak reverse voltage (T_j=25)	V_{RRM}	800	V
RMS on-state current (T_C 86)	$I_{T(RMS)}$	4	A
Non repetitive surge peak on-state current (full cycle , t_p=20ms , T_j=25)	I_{TSM}	40	A

Peak gate power	P_{GM}	5	W
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	3.5	kV

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D=12V R_L=33	- -	MAX.	5	mA
				10	
V_{GT}		ALL	MAX.	1	V
V_{GD}	V_D=V_{DRM} T_j=125 R_L=3.3k	ALL	MIN.	0.2	V
I_L	I_G=1.2I_{GT}	-	MAX.	15	mA
		-		25	

ORDERING INFORMATION



MARKING

FIG.1: Maximum power dissipation versus RMS

MS0n2-St(t)e1 cu(M)52 vsTte curS5 tersuse 0-2.8a(us)-2.8e ( )]TJ 0.002 Tw795.114 -1.469 T[9 (t)-4e(um)-3.3per (at)-4ure(

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards

ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		H- I- J	K			

PACKAGE MECHANICAL DATA

