ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value	Unit
I_{GT}	V_D			

ORDERING INFORMATION

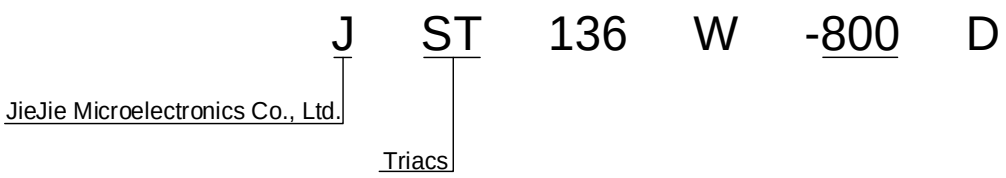


FIG.1: Maximum power dissipation versus RMS on-state current

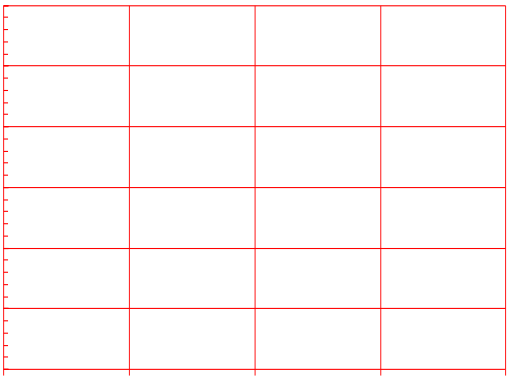


FIG.2: RMS on-state current versus case temperature

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

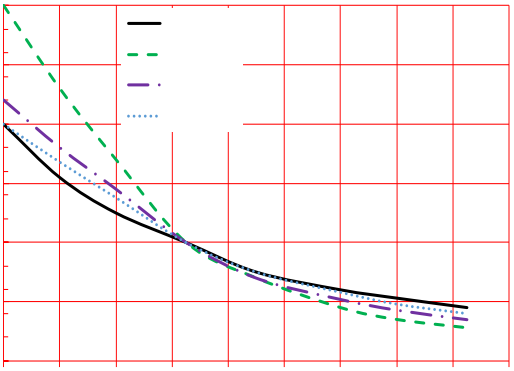
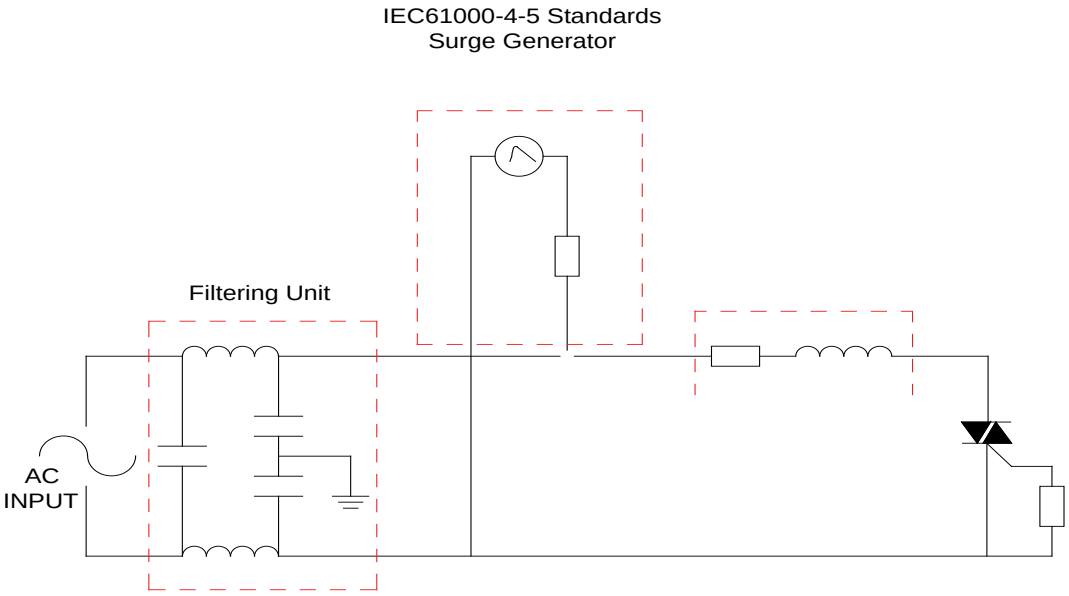


FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



	IGT(mA)	Package	Base qty. (pcs)	

PACKAGE MECHANICAL DATA

