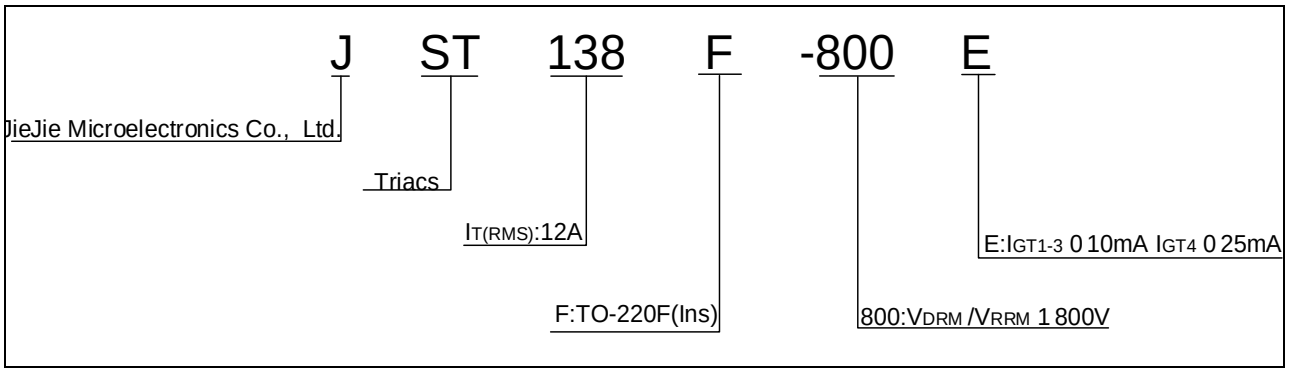
JST138F-800E 12A TRIAC

Rev.A.1.1

5 9{ /wLt ¢ L h b Y

h R 5 ERING IN C h RMATI h N

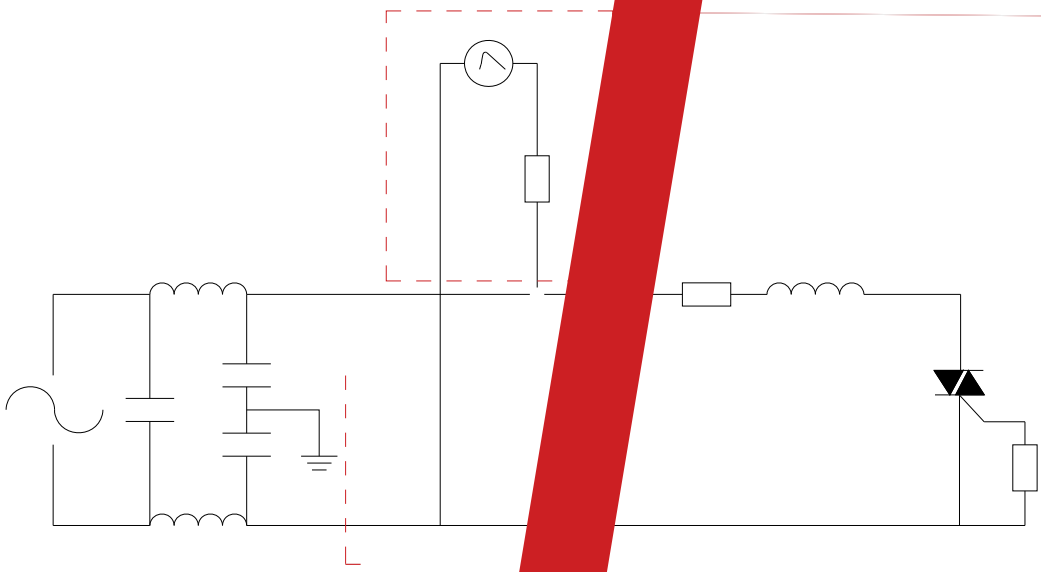


MAR Y ING

FIG.1: Maximum power dissipation versus RMS on-state current

FIG.2: RMS on-state current versus case temperature

FIG.7 ÖTest circuit for inductive and resistive lo o IEC-61000-4-5 standards



h R 5 ERING IN C h RMATI h N

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		- -				
JST138F-800E	**800**	**10**	**25**	**TO-220F(Ins)**	**50**	**Tube**

Document Revision History

Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated
Oct.15, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

PAC Y AGE MECHANICAL 5 ATA



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.
Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.

is a registered trademark of Jiangsu JieJie Microelectronics Co., Ltd.

Copyright © 2025 Jiangsu JieJie Microelectronics Co., Ltd. All rights reserved.