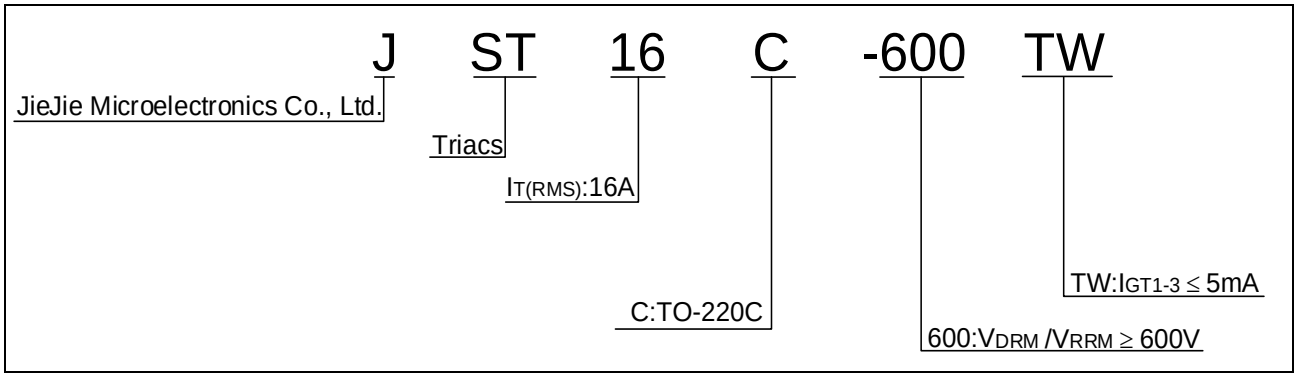
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	3	kV

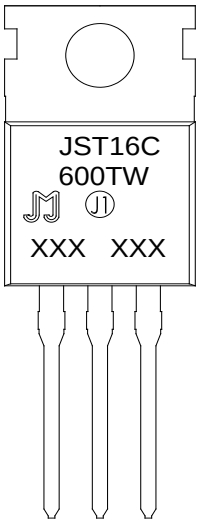
(T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D =12V R_L =33Ω	- -	MAX.	5	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	V_D =V_{DRM} T_j =125 R_L =3.3kΩ	- -	MIN.	0.2	V
I_L	I_G =1.2I_{GT}	-	MAX.	15	mA
				20	
I_H	I_T =500mA		MAX.	15	mA
dV/dt	V_D=400V Gate Open T_j =125		MIN.	300	V/μs
(dI/dt)c	(dV/dt)c=10V/μs T_j=125		MIN.	1.5	A/ms
t_{on}	I_G=10mA I_A=200mA I_R=20mA T_j=25		TYP.	3	μs
t_{off}				25	

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	I_{TM} =22.5A t_p=380μs	T_j=25	1.5	V
V_{TO}	Threshold voltage	T_j=125	0.77	V
R_D	Dynamic resistance	T_j=125	30	mΩ
I_{DRM}	V_D =V_{DRM} V_R =V_{RRM}	T_j=25	5	μA
I_{RRM}		T_j=125	0.4	mA

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	1.1	/W
$R_{th(j-a)}$	junction to ambient (AC)	60	/W


J
ST
16
C
-600
TW
JieJie Microelectronics Co., Ltd.
Triacs
IT(RMS):16A
C:TO-220C
600:VDRM /VRRM ≥ 600V
TW:IGT1-3 ≤ 5mA


JST16C
600TW
J1
XXX XXX

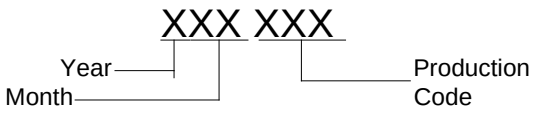

XXX XXX
Year
Month
Production
Code

FIG.1: Maximum power dissipation versus RMS on-state current

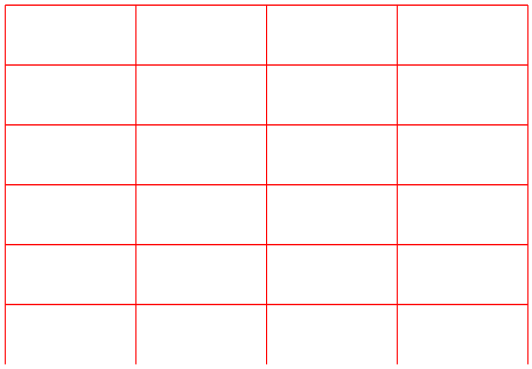
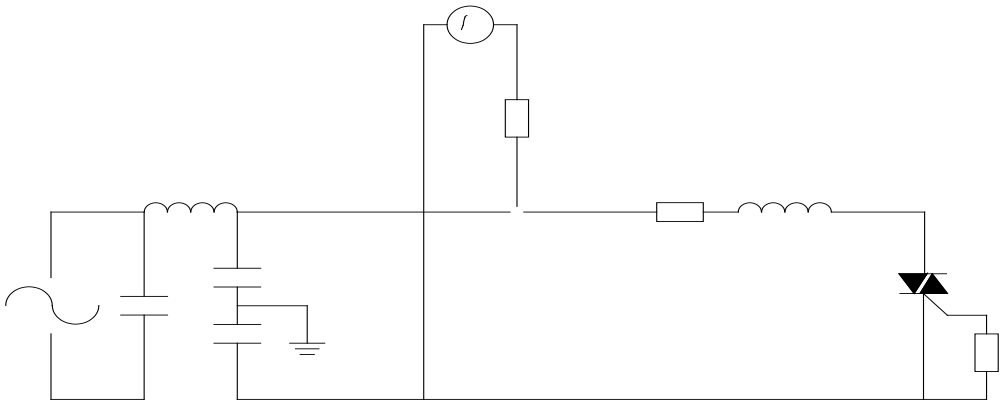


FIG.2: RMS on-state current versus case temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		H- I- J			
JST16C-600TW	600	5	TO-220C	50	Tube

Document Revision History

Date	Revision	Changes
Apr.12, 2023	A.1.0	Last updated
Oct.15, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA
Sept.16, 2026	A.1.2	Revise I_{TSM}, dV/dt

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