JST20E-600CW

FIG.1: Maximum power dissipation versus RMS on-state current

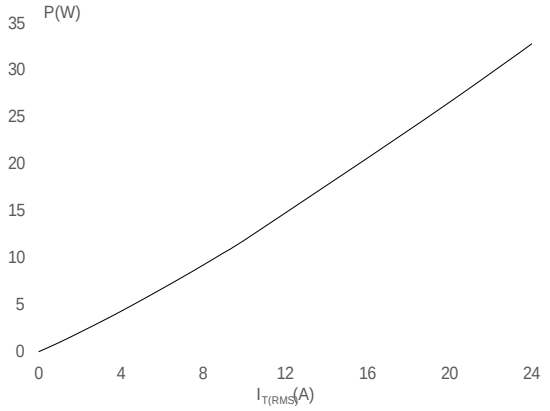


FIG.2: RMS on-state current versus case temperature

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

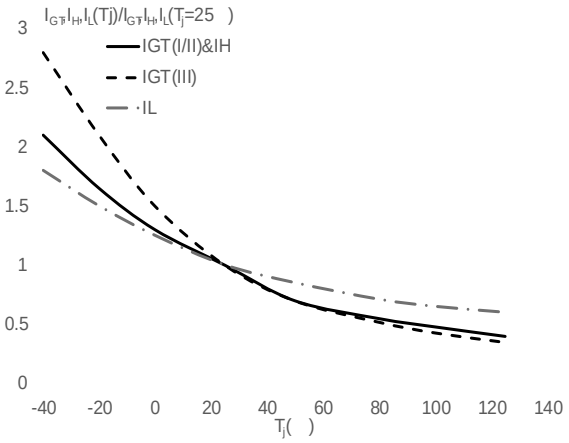


FIG.8:

ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
JST20E-600CW	600	35	TO-263	50	Tube
JST20E-600CW-TR				800	Tape & Reel

Document Revision History

Date	Revision	Changes
Apr.12, 2023	A.1.0	Last updated
Oct.20, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for the consequences