Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.8)	V_{pp}	3	kV

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D =12V R_L =33Ω	- -	MAX.	10	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	V_D =V_{DRM} T_j =125 R_L =3.3kΩ	- -	MIN.	0.2	V
I_L	I_G =1.2I_{GT}	-	MAX.	20	mA
				35	
I_H	I_T =100mA		MAX.	15	mA
dV/dt	V_D=540V Gate Open T_j =125		MIN.	500	V/μs
(dI/dt)c	(dV/dt)c=10V/μs, T_j=125		MIN.	2.5	A/ms
t_{on}	I_G=20mA I_A=200mA I_R=20mA T_j=25		TYP.	2.5	μs
t_{off}				25	

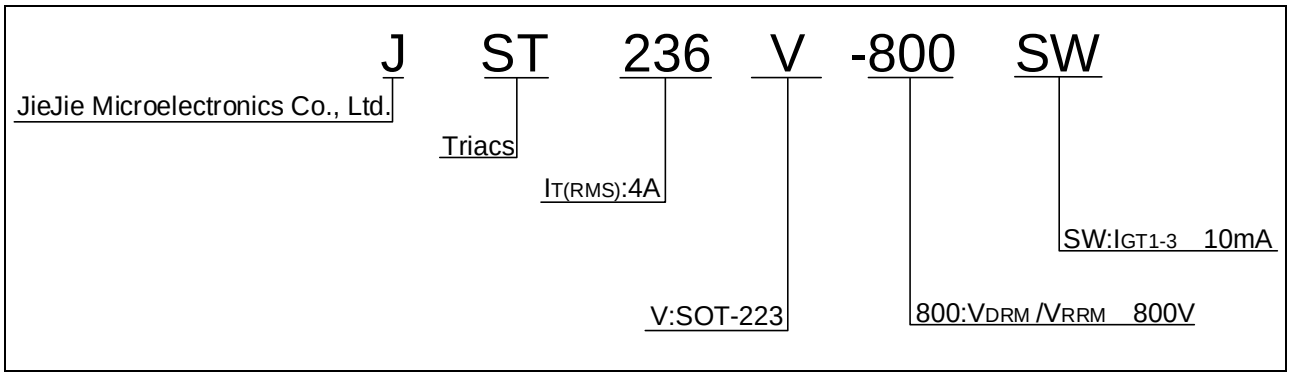
STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	I_{TM} =5A t_p=380μs	T_j=25	1.5	V
V_{TO}	Threshold voltage	T_j=125	0.78	V
R_D	Dynamic resistance	T_j=125	190	mΩ
I_{DRM}	V_D =V_{DRM} V_R =V_{RRM}	T_j=25	5	μA
I_{RRM}		T_j=125	0.25	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	17	/W
$R_{th(j-a)}$	junction to ambient (AC)	60	/W

ORDERING INFORMATION



MARKING

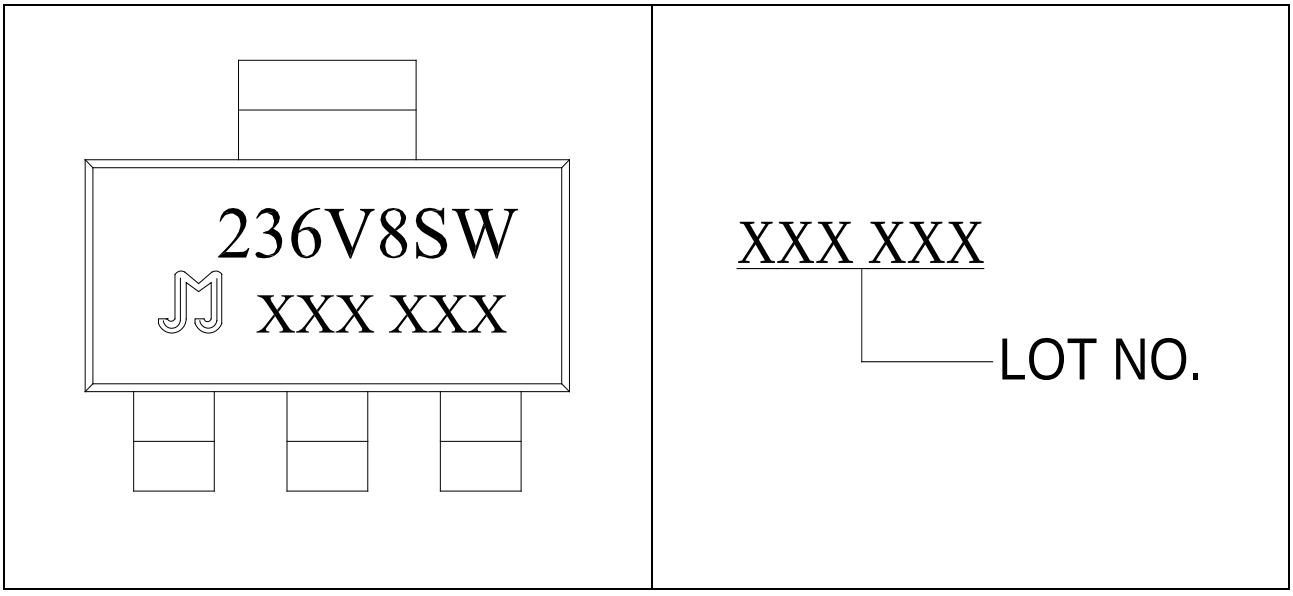


FIG.1: Maximum power dissipation versus RMS on-state current

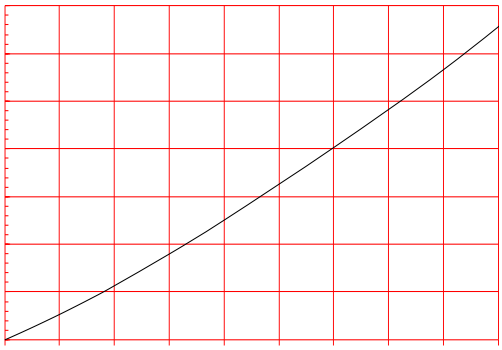


FIG.2: RMS on-state current versus case temperature

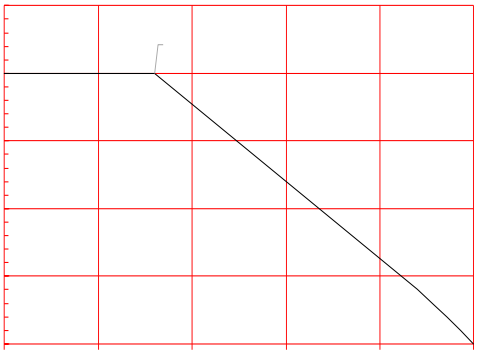


FIG.3: RMS on-state current versus ambient temperature (printed circuit board FR4,copper thickness:35μm)(full cycle)

FIG.4: Surge peak on-state current versus number of cycles

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

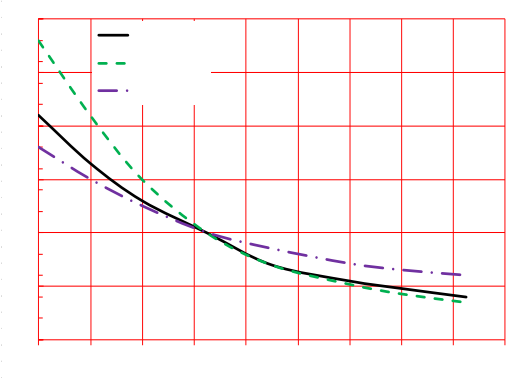
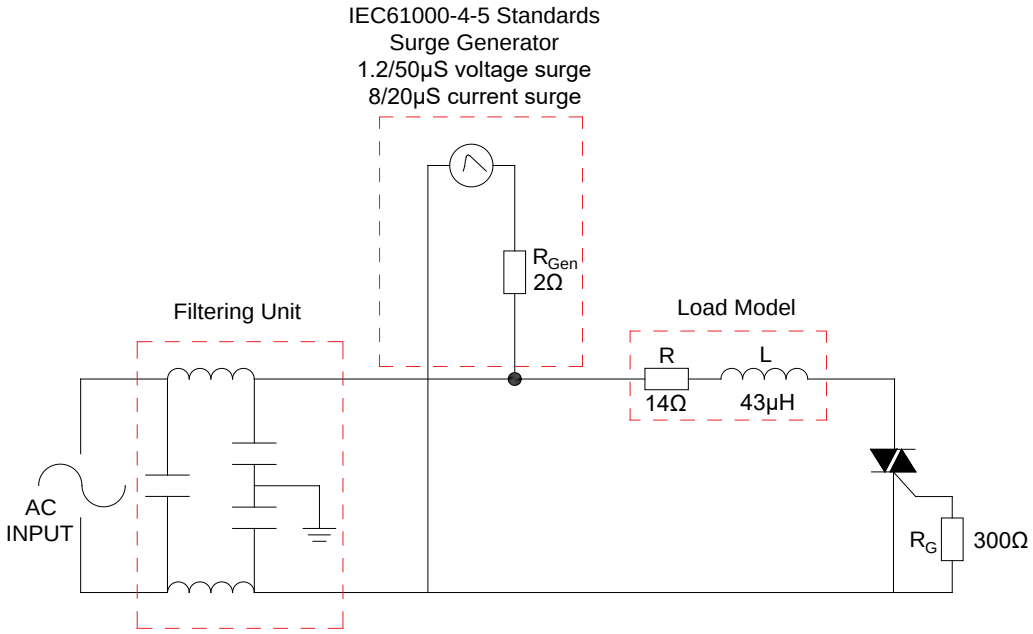


FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
JST236V-800SW	800	10	SOT-223	4,000	Tape & Reel

Document Revision History

Date	Revision	Changes
Apr.11, 2025	A.1.0	Last updated

DELIVERY MODE

Dimensions

Ref.

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