+86-513-68528666
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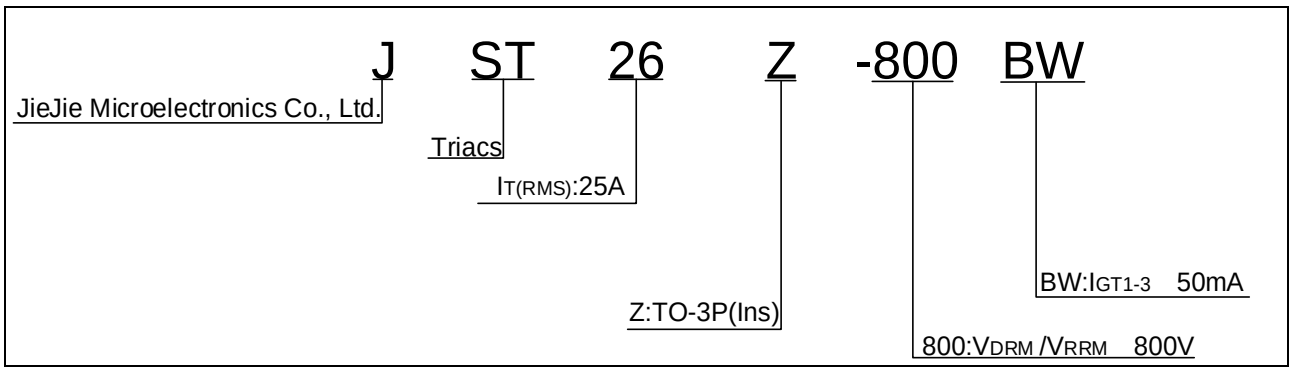
Peak gate power	P_{GM}	10	W
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	5	kV

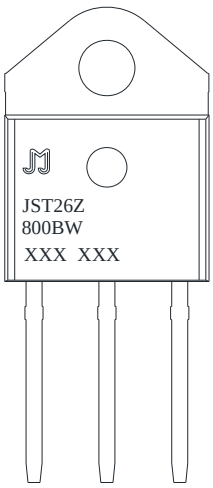
(T_j=25 unless otherwise specified)

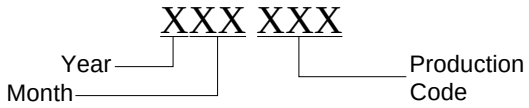
Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D =12V R_L =33Ω	- -	MAX.	50	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	V_D =V_{DRM} T_j=125 R_L =3.3kΩ	- -	MIN.	0.2	V
I_L	I_G=1.2I_{GT}	-	MAX.	80	mA
				100	
I_H	I_T =500mA		MAX.	75	mA
dV/dt	V_D=540V Gate Open T_j=125		MIN.	2000	V/μs
(dI/dt)c	(dV/dt)c=20V/μs T_j=125		MIN.	25	A/ms
t_{on}	I_G=80mA I_A=400mA I_R=40mA T_j=25		TYP.	10	μs
t_{off}				70	

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	I_{TM} =35A t_p=380μs	T_j=25	1.5	V
V_{TO}	Threshold voltage	T_j=125	0.73	V
R_D	Dynamic resistance	T_j=125	25	mΩ
I_{DRM}	V_D=$V_{DRM}$$V_R$ =V_{RRM}	T_j=25	5	μA
I_{RRM}		T_j=125	2	mA

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	1.1	/W
$R_{th(j-a)}$	junction to ambient (AC)	50	/W


J
ST
26
Z
-800
BW
JieJie Microelectronics Co., Ltd.
Triacs
IT(RMS):25A
Z:TO-3P(Ins)
BW:IGT1-3 50mA
800:VDRM /VRRM 800V


JST26Z
800BW
XXX XXX


XXX XXX
Year
Month
Production
Code

JST26Z-800BW

Order code	Voltage V_{DRM}/V_{RRM}(V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
JST26Z-800BW	800	50	TO-3P(Ins)	30	Tube

Document Revision History

Date	Revision	Changes
Apr.12, 2023	A.1.0	Last update
Oct.16, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA



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