Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.8)	V_{pp}	2.5	kV

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value	Unit	
I	V =12V R_L =33	- -	MAX.	50	mA
V		- -	MAX.	1.3	V
V	V =V_{DRM} T_j =125 R_L =3.3k	- -	MIN.	0.15	V
I_L	I_G =1.2I_{GT}	-	MAX.	80	mA
				100	
I_H	I_T =500mA		MAX.	75	mA
dV/dt	V_D=540V Gate Open T_j =125		MIN.	2000	V s
(dI/dt)c	G 9 G W F j=125 V 7		MIN.	25	A/ms
t_{on}	I_G=80mA I_A=400mA I_R=40mA T_j=25		TYP.	10	s
t_{off}				70	

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX .)	Unit
V_{TM}	I_{TM} =42A t_p=380 s	T_j=25	1.5	V
V_{TO}	Threshold voltage	T_j=125	0.72	V
R_D	Dynamic resistance	T_j=125	25	m
I_{DRM}	V_D =V_{DRM} V_R =V_{RRM}	T_j=25	5	A
I_{RRM}		T_j=125	2	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	0.7	/W
$R_{th(j-a)}$	junction to ambient (AC, in free air, S=2cm^2)	45	/W

ORDERING INFORMATION

J	ST	30	E	-800	BW	-/
JieJie Microelectronics Co., Ltd.	Triacs	$I_{T(RMS)}$:30A	E:TO-263	800:V_{DRM} /V_{RRM} ≥ 800V	BW:I_{GT1-3} ≤ 50mA	Blank:Tube -TR:Tape & Reel

MARKING

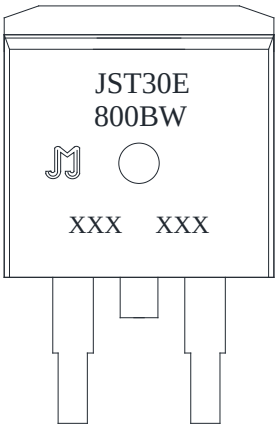


FIG.1: Maximum power dissipation versus RMS on-state current

FIG.2: RMS on-state current versus case temperature

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

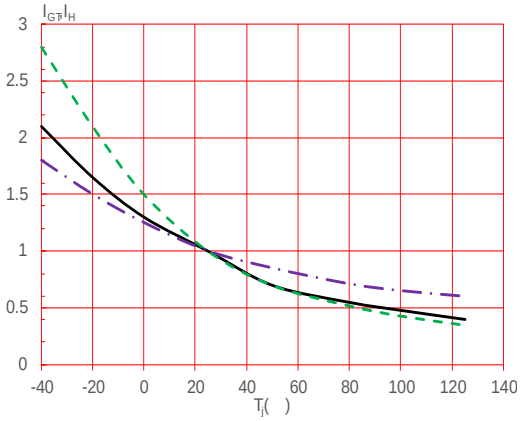
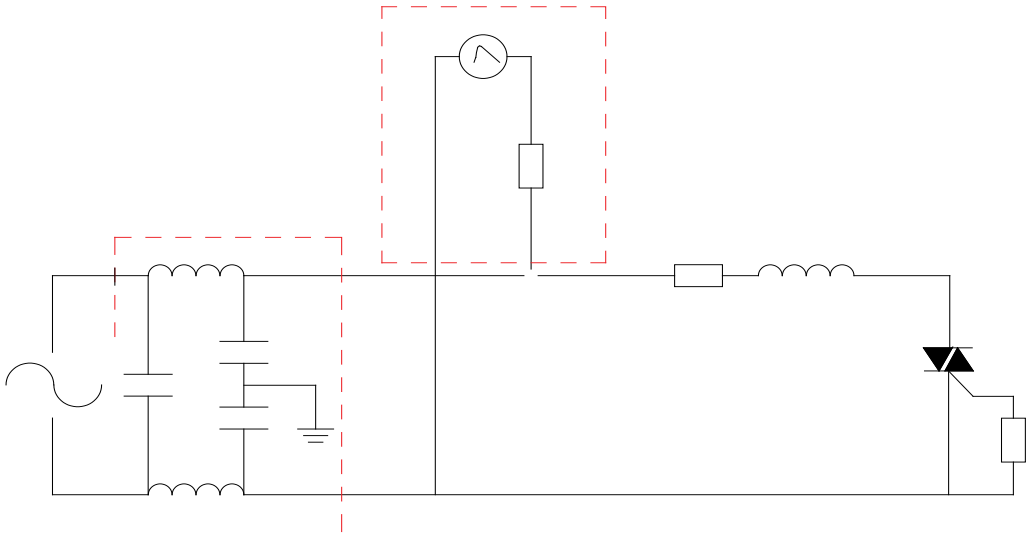
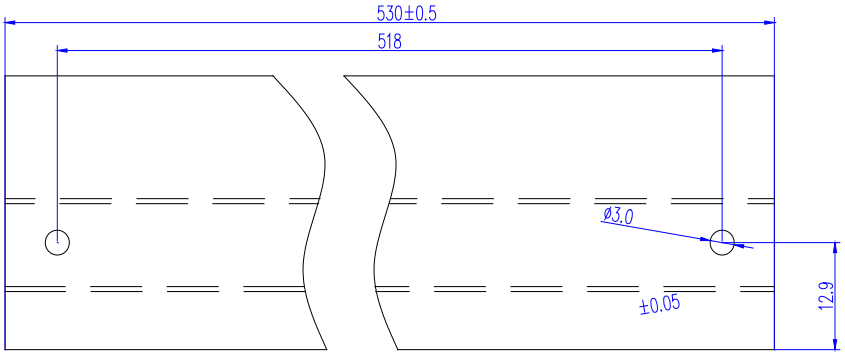
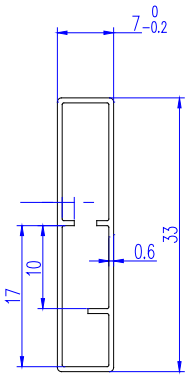


FIG.8: Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



DELIVERY MODE





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