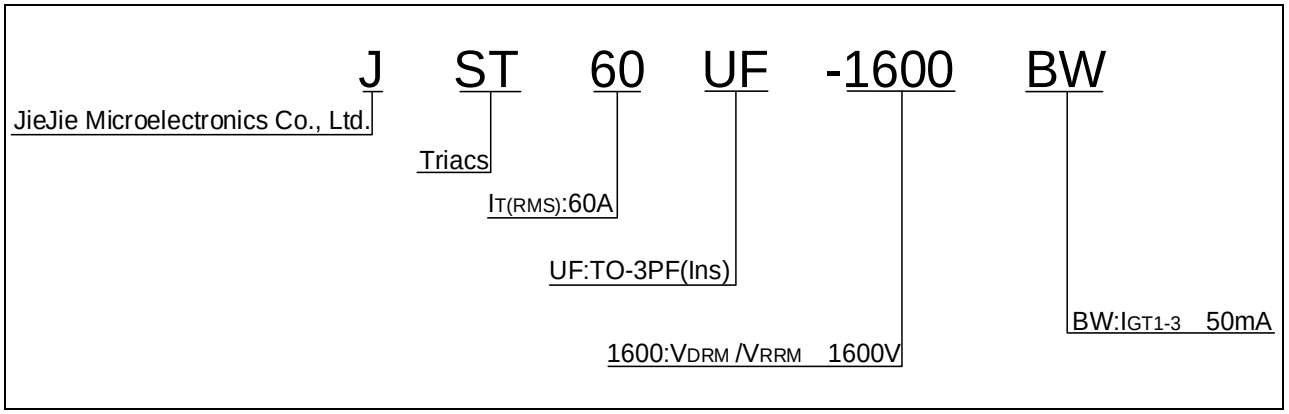
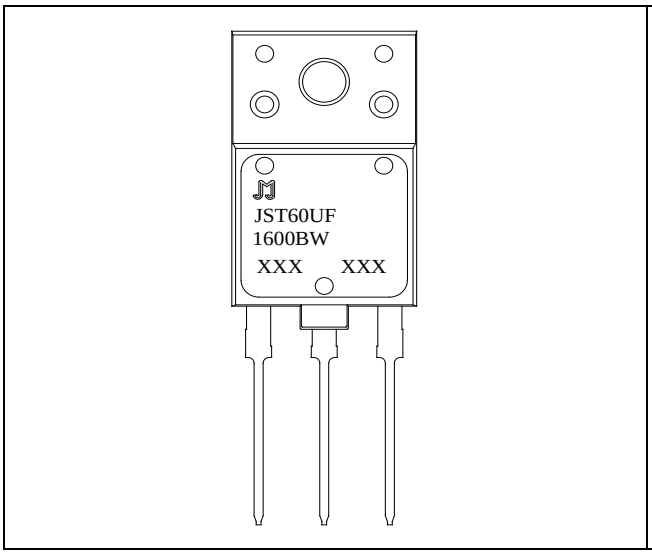
ORDERING INFORMATION



MARKING



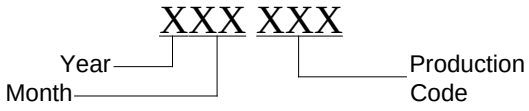


FIG.1: Maximum power dissipation versus RMS on-state current

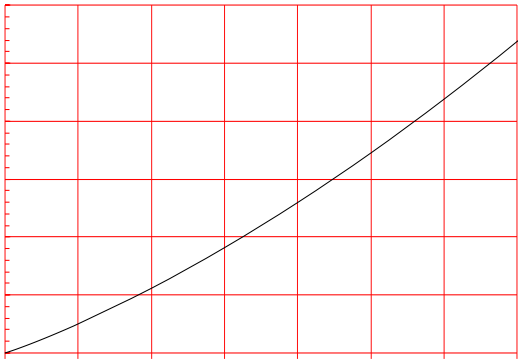


FIG.2: RMS on-state current versus case temperature

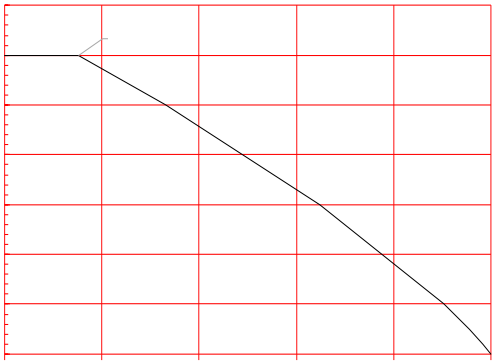


FIG.3: Surge peak on-state current versus number of cycles

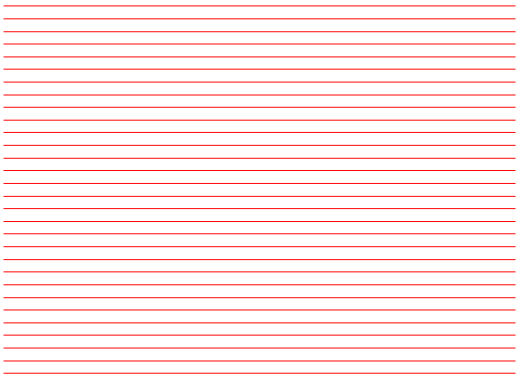


FIG.4: On-state characteristics

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM}(V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
JST60UF-1600BW	1600	50	TO-3PF(Ins)	30	Tube

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