

FIG.1 Transient thermal impedance junction to case(per thyristor or diode)

FIG.2 Maximum power dissipation (per thyristor or diode)

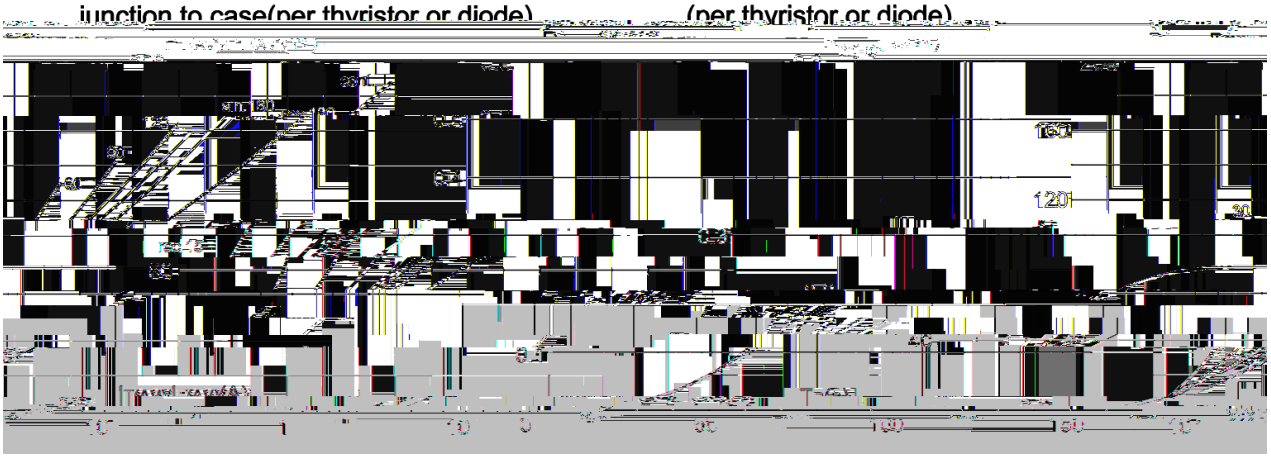


FIG.3 Relative variations of gate trigger current, holding

FIG.4 Forward characteristics

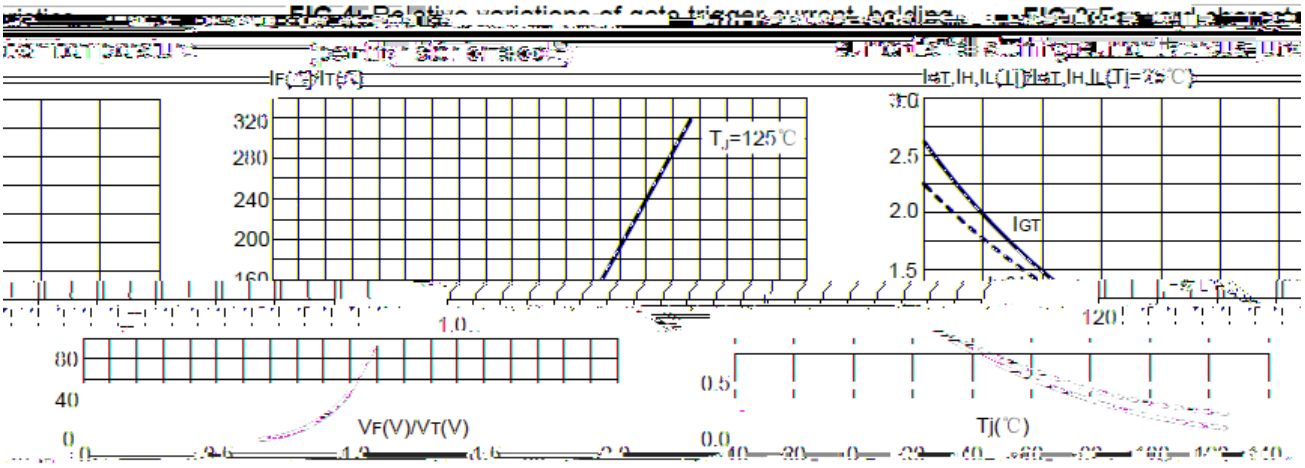


FIG.5 Gate trigger characteristics

FIG.6: Forward current vs. case temperature