- Half-bridge SCR configuration integrated in a single package
- High-thermal-conductivity DBC insulation for excellent heat dissipation
- Vacuum soldering technology for enhanced reliability

- Heating control
- Light control system
- DC motor

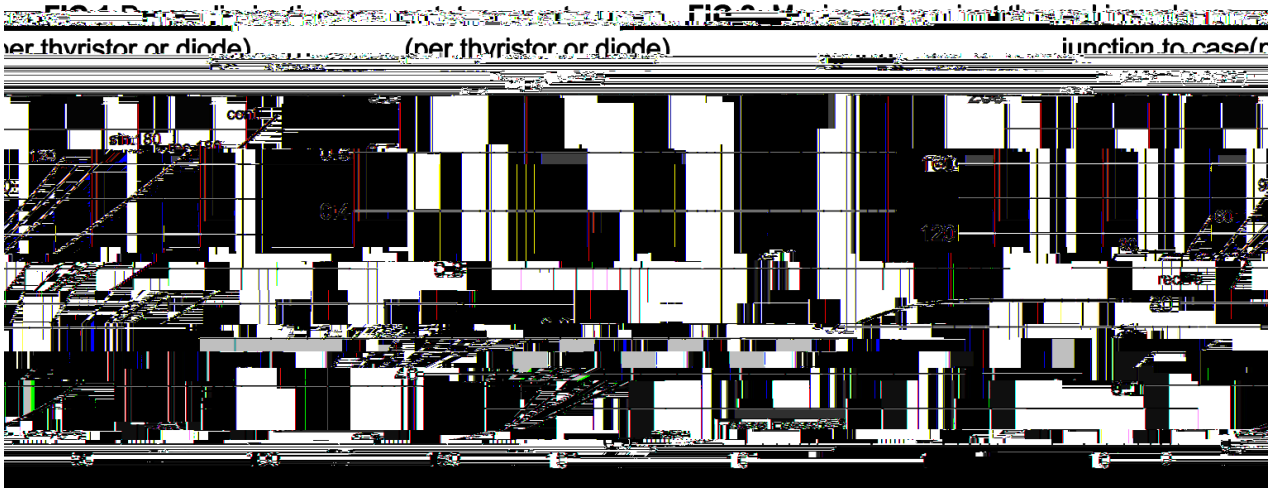
(@ T_C = 25℃ unless otherwise specified)

Repetitive peak off-state voltage	Tvj = 25℃	V_{DRM}	1600	V
Repetitive peak reverse voltage	Tvj = 25℃	V_{RRM}	1600	V

Non-repetitive peak off-

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JMT110KT16T1	JMT110KT16T1	T1	100g 5/PCS	10 PCS	120 PCS



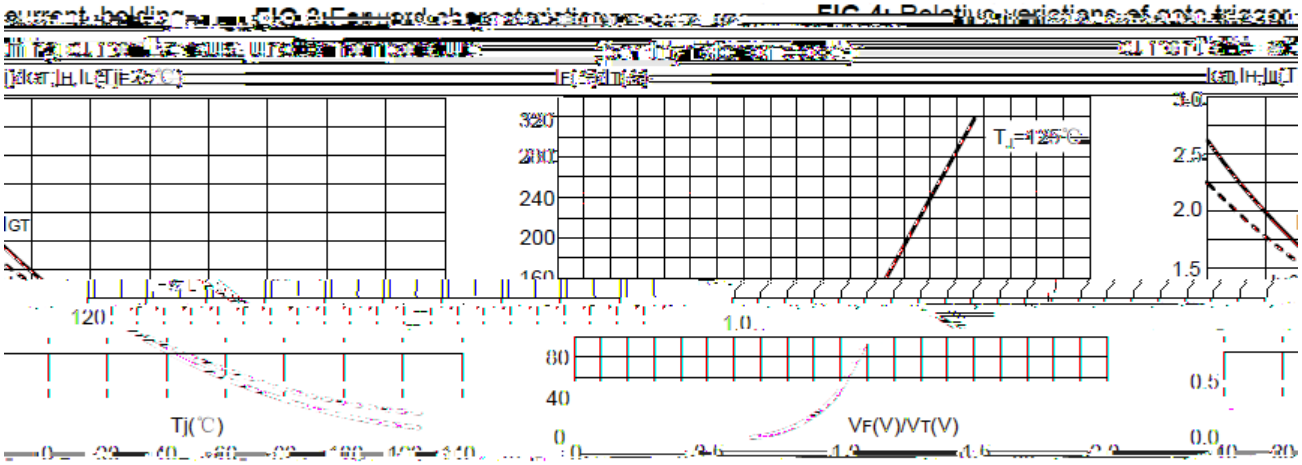
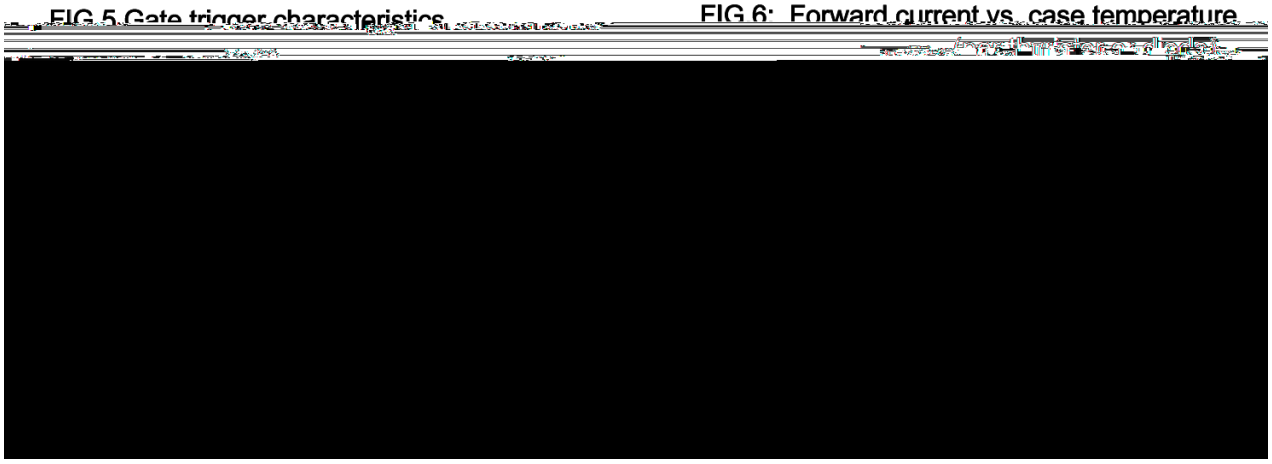


FIG 5 Gate trigger characteristics

FIG 6: Forward current vs. case temperature



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