

FIG 1: Power dissipation (per thyristor or diode)

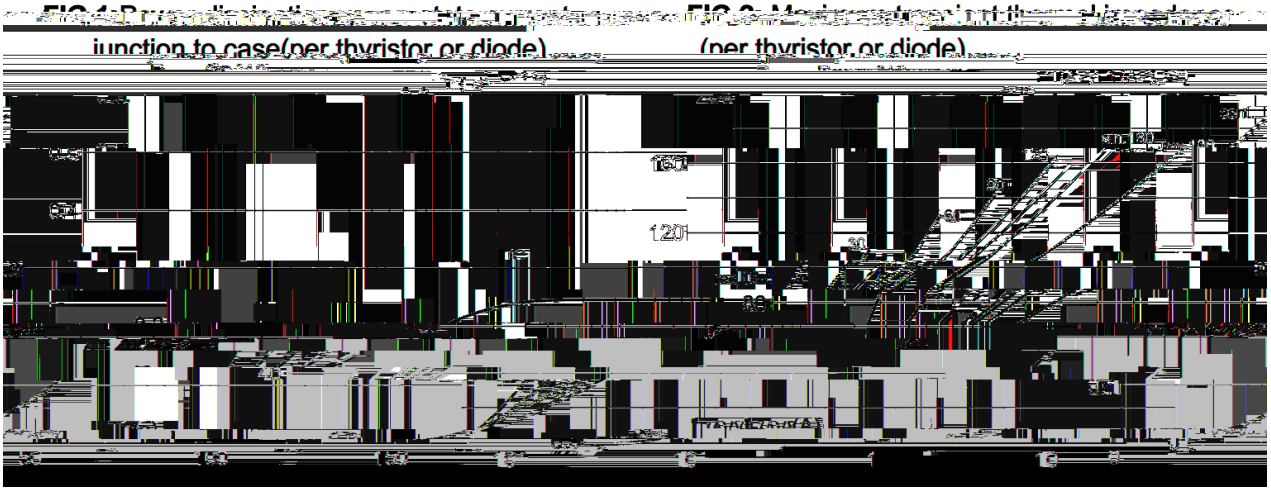


FIG 2: Maximum transient thermal impedance junction to case(per thyristor or diode)

FIG 3: Forward characteristics

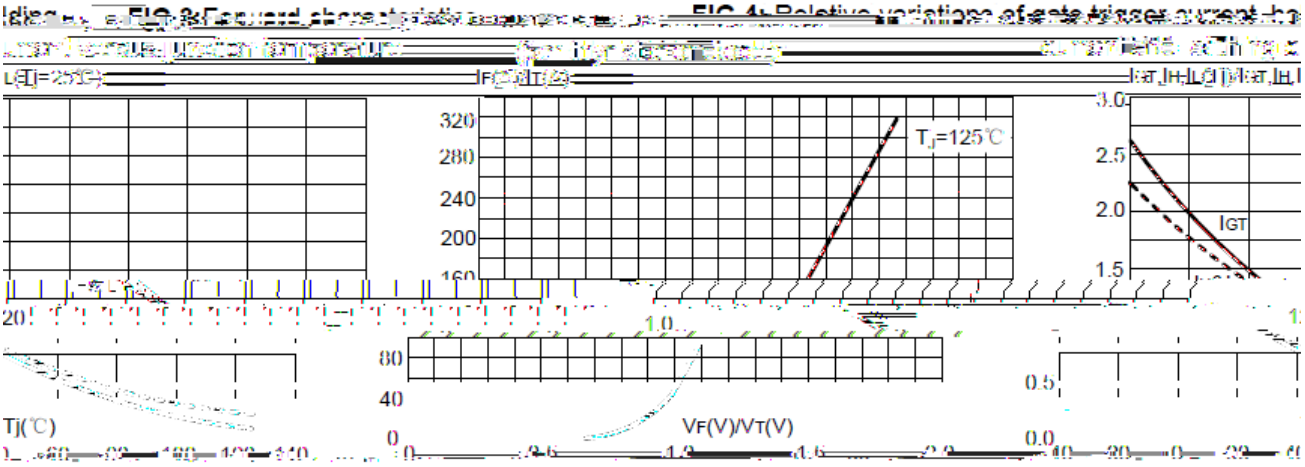


FIG 4: Relative variations of gate trigger current

FIG 5 Gate trigger characteristics

FIG 6: Forward current vs. case temperature

